



PCB@

PCB GH53G LA-L191P LS-L031P/K852P/K853P
DAZ30A00101



LOGO@

ROYALTY HDMI W/LOGO+HDCP
RC0000003HM



X4E@

SMT EMC EG0 G1 G1R FOR EE AL191 GH53G
X4EAR4BOL01



X4EFP@

SMT EMC EG0 G1 G1R FOR EE AL191 GH53G
X4EAR4BOL21

Compal Confidential

MB Schematic Document

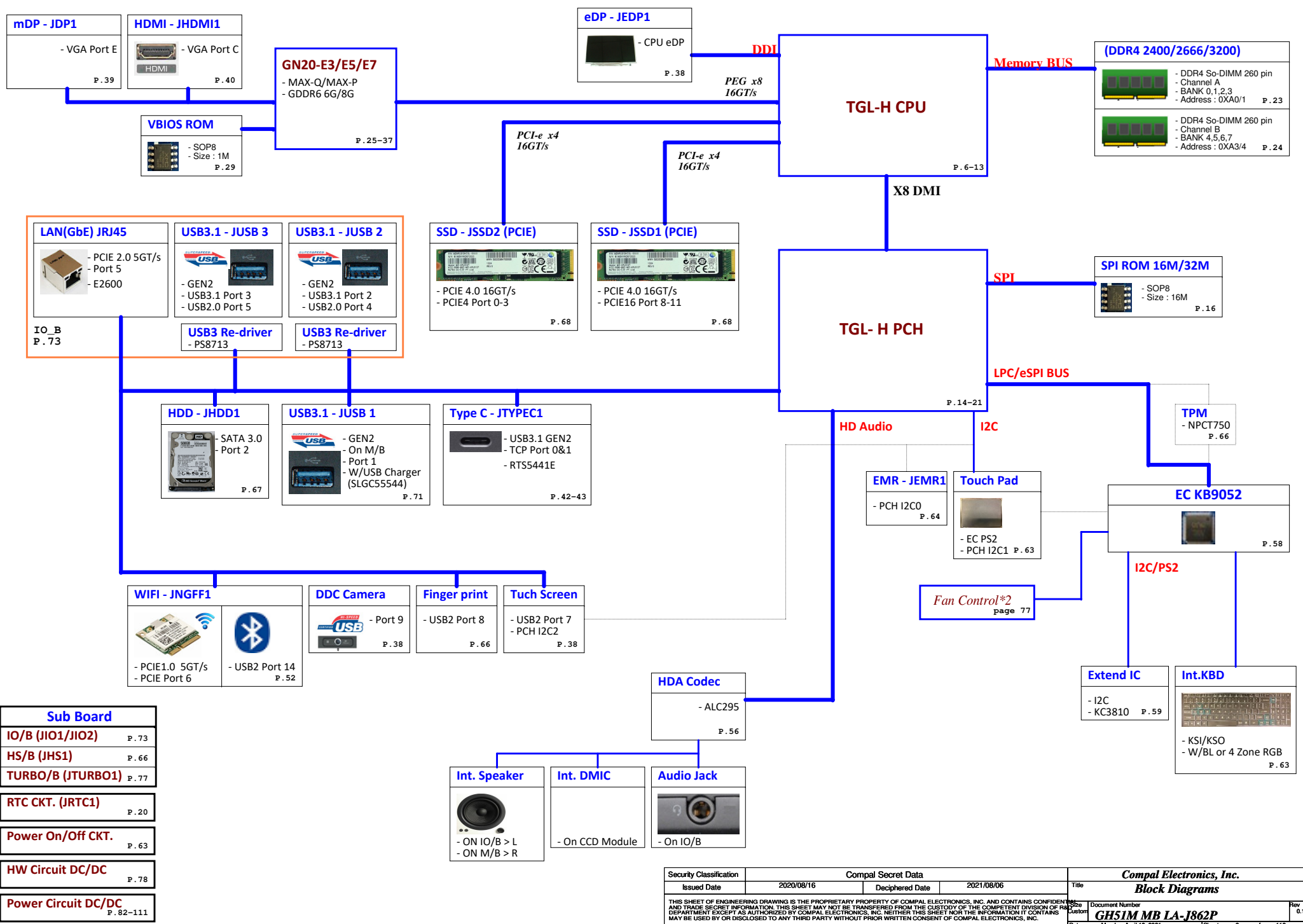
GH51G/GH53G/GH57G/GH71G/GH73G

LA-L191P

Rev:1.A

2020.04.12

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|--|--------------------|-----------------|------------|-----------|---------------------------------|----------------|
| Security Classification | Compal Secret Data | | Title | | Compal Electronics, Inc. | |
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| | | | | | GH51M MB LA-J862P | 0.1 |
| | | | | Date: | Monday, April 12, 2021 | Sheet 1 of 112 |



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|---|--------------------|-----------------|------------|---|
| Security Classification | Compal Secret Data | | Title | |
| Issued Date | 2020/08/16 | Deciphered Date | 2021/08/06 | Block Diagrams |
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| Size Custom Date: Monday, April 12, 2021 | | | | Rev 0.1 Sheet 2 of 112 |

| Vcc | 3.3V +/- 5% | EC Board ID Table for AD channel | | | | | Board ID | PCB Revision |
|------|-------------|----------------------------------|---------|---------|-------------|-------------|----------|--------------|
| Ra | 100K +/- 1% | Rb | Vmin | Vtyp | Vmax | EC AD | | |
| 0 | 0 | | 0.000 V | 0.300 V | 0x00 - 0x13 | SD034120280 | | |
| 1 | 12K +/- 1% | 0.347 V | 0.345 V | 0.360 V | 0x14 - 0x1E | SD034150280 | | |
| 2 | 15K +/- 1% | 0.423 V | 0.430 V | 0.438 V | 0x1F - 0x25 | SD034200280 | | |
| 3 | 20K +/- 1% | 0.541 V | 0.550 V | 0.559 V | 0x26 - 0x30 | SD034270280 | | |
| 4 | 27K +/- 1% | 0.691 V | 0.702 V | 0.713 V | 0x31 - 0x3A | SD034330280 | | |
| 5 | 33K +/- 1% | 0.807 V | 0.819 V | 0.831 V | 0x3B - 0x45 | SD034430280 | | |
| 6 | 43K +/- 1% | 0.978 V | 0.992 V | 1.006 V | 0x46 - 0x54 | SD034560280 | | |
| 7 | 56K +/- 1% | 1.169 V | 1.185 V | 1.200 V | 0x55 - 0x64 | SD034750280 | | |
| 8 | 75K +/- 1% | 1.398 V | 1.414 V | 1.430 V | 0x65 - 0x76 | SD034100380 | | |
| * 9 | 100K +/- 1% | 1.634 V | 1.650 V | 1.667 V | 0x77 - 0x87 | SD034130380 | | |
| 10 | 130K +/- 1% | 1.849 V | 1.865 V | 1.881 V | 0x88 - 0x96 | SD034160380 | | |
| 11 | 160K +/- 1% | 2.015 V | 2.031 V | 2.046 V | 0x97 - 0xA4 | SD034200380 | | |
| 12 | 200K +/- 1% | 2.185 V | 2.200 V | 2.215 V | 0xA5 - 0xAF | SD000001B80 | | |
| * 13 | 240K +/- 1% | 2.316 V | 2.329 V | 2.343 V | 0xB0 - 0xB7 | SD000002B80 | | |
| 14 | 270K +/- 1% | 2.395 V | 2.408 V | 2.421 V | 0xB8 - 0xBF | SD034330380 | | |
| 15 | 330K +/- 1% | 2.521 V | 2.533 V | 2.544 V | 0xC0 - 0xC9 | SD000000M80 | | |
| 16 | 430K +/- 1% | 2.667 V | 2.677 V | 2.687 V | 0xCA - 0xD4 | SD034560380 | | |
| 17 | 560K +/- 1% | 2.791 V | 2.800 V | 2.808 V | 0xD5 - 0xDD | SD000000A80 | | |
| 18 | 750K +/- 1% | 2.905 V | 2.912 V | 2.919 V | 0xDE - 0xF0 | | | |
| 19 | NC | 3.000 V | 3.000 V | | 0xF1 - 0xFF | | | |

| Board ID | PCB Revision |
|----------|----------------------|
| 0 | 50 Rev0.1 |
| 1 | 50 Rev0.2 |
| 2 | 50 Rev1.0 |
| 3 | |
| 4 | 50 Rev0.1+RGB |
| 5 | 50 Rev0.2+RGB |
| 6 | 50 Rev1.0+RGB |
| 7 | |
| 8 | 60 Rev0.1 |
| 9 | 60 Rev0.2/1.0/1A |
| 10 | |
| 11 | |
| 12 | 60 Rev0.1+RGB |
| 13 | 60 Rev0.2/1.0/1A+RGB |
| 14 | |
| 15 | |
| 16 | |
| 17 | |
| 18 | |
| 19 | |

For PoP BOM 4/12
 4400 8V2 setting
 ID:13 (4408)
 ID:13 (4408)

*PCB Version
 *Key board type

| STATE | SIGNAL | SLP_S3# | SLP_S4# | SLP_S5# | +VALW | +V | +VS |
|----------------------|--------|---------|---------|---------|-------|-----|-----|
| S0 (Full ON) | HIGH | HIGH | HIGH | ON | ON | ON | ON |
| S3 (Suspend to RAM) | LOW | HIGH | HIGH | ON | ON | ON | OFF |
| S4 (Suspend to Disk) | LOW | LOW | HIGH | ON | OFF | OFF | OFF |
| S5 (Soft OFF) | LOW | LOW | LOW | ON | OFF | OFF | OFF |

| Power Plane | Description | S0 | S3 | S4 | S5 |
|-----------------|--|-----|-----|-----|-----|
| +RTCVCC | RTC Battery Power | ON | ON | ON | ON |
| +19V_VIN | Adapter power supply | N/A | N/A | N/A | N/A |
| +12.6V_BATT | Battery power supply | N/A | N/A | N/A | N/A |
| +19VB | AC or battery power rail for power circuit. | N/A | N/A | N/A | N/A |
| +3VLP | +19VB to +3VLP power rail for suspend power | ON | ON | ON | ON |
| +5VALW | +5V Always power rail | ON | ON | ON | ON |
| +3VALW | System +3VALW always on power rail | ON | ON | ON | ON* |
| +3VALW_DSW | +3VALW power for PCH DSW rails | ON | ON | ON | ON* |
| +1.05VALW | +1.05V Always power rail | ON | ON | ON | ON |
| +1.2V_VDDQ | DDR4 +1.2V power rail | ON | ON | OFF | OFF |
| +1.05V_VCCST | Sustain voltage for processor in Standby modes | ON | ON | OFF | OFF |
| +5VS | System +5V power rail | ON | OFF | OFF | OFF |
| +3VS | System +3V power rail | ON | OFF | OFF | OFF |
| +1.05VS_VCCSTG | +1.05VALW_PRIM Gated version of VCCST | ON | OFF | OFF | OFF |
| +0.6VS_VTT | DDR +0.6VS power rail for DDR terminator . | ON | OFF | OFF | OFF |
| +VCC_CORE | Core voltage for CPU | ON | OFF | OFF | OFF |
| +VCC_GT | Sliced graphics power rail | ON | OFF | OFF | OFF |
| +VCCIO | CPU IO +0.95VS power rail | ON | OFF | OFF | OFF |
| +VCC_SA | System Agent power rail | ON | OFF | OFF | OFF |
| +1.8VSDGPU_AON | +1.8VS power rail for GPU(AON rails) | ON | OFF | OFF | OFF |
| +1.8VSDGPU_MAIN | +1.8VS power rail for GPU GC6 | ON | OFF | OFF | OFF |
| +NVVDD1 | Core voltage for VGA (merge core & core_s) | ON | OFF | OFF | OFF |
| +1.35VSDGPU | +1.35VS power rail for GPU | ON | OFF | OFF | OFF |
| +1.0VSDGPU | +1.0VS power rail for GPU | ON | OFF | OFF | OFF |
| +1.8VALW | System +1.8VALW always on power rail | ON | ON | ON | ON* |

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

| BUS | Device | Address(7 bit) | Address(8bit) | | |
|------------------------|-----------------------------|----------------|---------------|------------|--|
| | | | Write | Read | |
| I2C_1 (+3V PRIM) | TM-P3393-003 (Touch Pad) | | | | |
| I2C_5 (+3VS) | (Touch Screen) | | | | |
| PCH_SMBCLK (+3VS) | DIMM1 | | | | |
| | DIMM2 | | | | |
| PCH_SML0CLK (+1.8VALW) | BURNSIDE-BRIDGE 1 | 0x53 | | | |
| | BURNSIDE-BRIDGE 2 | 0x54 | | | |
| PCH_SML1CLK (+1.8VALW) | PD PTPS65991 | 0x21 | | | |
| | (EC) | | | | |
| EC_SMB_CK0 (+3VLP) | BQ24780 (Charger IC) | 0x12 | | | |
| | BATTERY PACK | 0x16 | | | |
| EC_SMB_CK1 (+3VLP_EC) | GN20 E3/E5/E7 (VGA) | 0x9E | | | |
| | Thermal Sensor (NCT7718W) | 1001_100xb | 1001_1001b | 1001_1000b | |
| | Thermal Sensor (G781) (PCH) | 1001_1010b | 1001_1010b | 1001_1010b | |
| EC_SMB_CK2 (+5VS) | LED driver | 0xC0 | | | |
| | (PER KEY) | | | | |

KC3810 0xC0

| Item (X43) | BOM Structure | Item (X43) | BOM Structure |
|-----------------------|---------------|-------------------|---------------|
| Unpop | @ | eDP-TS USB | TS_USB@ V |
| Connector | CONN@ | eDP-TS I2C | TS_I2C@ V |
| PCB | PCB@ V | USB C circuit | TypeC@ V |
| UMA Only(Reserved) | UMA@ | mDP | DP@ V |
| Board ID | EVT@ V | For Acer IOAC | IOAC@ V |
| Board ID RGB | EVTRGB@ V | Intel CNVi | CNVi@ V |
| CML i5 CPU QWCC | QWCC@ V | | |
| CML i7 CPU QWCB | QWCB@ V | FOR UART debug | UART@ V |
| TGL_PCH | HMS70@ V | Extend GPIO | KC3810@ V |
| dGPU circuit | VGA@ V | Finger Print | FP@ V |
| GPU_GN20_E3 | E3_PS@ V | KB backlight | KBLED@ V |
| GPU_GN20_E5 | E5_PS@ V | KB LED driver | LED14P@ V |
| GPU_GN20_E7 | E7_PS@ V | CMC signal | CMC@ V |
| | | Thermal sensor | TMS@ V |
| Intel request reserve | Glitch@ V | TPM pop | TPM@ V |
| Intel request reserve | @Glitch@ | | |
| INTEL ES sample issue | ES@ V | OVRM pop | OVRM@ V |
| | | OVRM GEN1 pop | GEN1@ V |
| 1 SPI device | 1LD@ V | UPI OVRM GEN1 pop | UPI_GEN1@ V |
| 1 SPI device | 2LD@ V | OVRM GEN2 pop | GEN2@ V |
| 32 MB Rom part | 32MB@ V | ON OVRM GEN2 pop | ON_GEN2@ V |
| 16 MB Rom part | 16MB@ V | UPI OVRM GEN2 pop | UPI_GEN2@ V |
| | | 12VFAN POWER | 12VFAN@ V |
| PREM POWER RAIL | PREM@ V | 5VFAN POWER | 5VFAN@ V |
| VOL POWER RAIL | VOL@ V | | |
| PERKEY CIRCUIT | PERKEY@ V | HDMI ROYALTY | LOGO@ V |

| 43 Level | Description | BOM Structure |
|-------------|--|---|
| 431AR4BOL01 | SMT MB AL191 GH51G GN20 E3 6G CC BRL | VGAR/SypwC8/TS_USB/TPM/TSGB/RFB/PSM/PCB/NFERKBYE/NCC3810B/NPE/KBLBDE/IOAC/GL1C8R/FP/ESB/DP/CNV1/CNCR/SVFN8/2LD/16M/K4E/GEN1/OVRM/ES_P8/QWCC/EVTRGB/LED14P/X7E5_S80B |
| 431AR4BOL04 | SMT MB AL191 GH51G GN20 E3 6G CB B3L | VGAR/SypwC8/TS_USB/TPM/TSGB/RFB/PSM/PCB/NFERKBYE/NCC3810B/NPE/KBLBDE/IOAC/GL1C8R/FP/ESB/DP/CNV1/CNCR/SVFN8/2LD/16M/K4E/GEN1/OVRM/ES_P8/QWCC/EVTRGB/LED14P/X7E5_S80B |
| 431AR4BOL05 | SMT MB AL191 GH51G GN20 E5 Q 8G CC BRL | VGAR/SypwC8/TS_USB/TPM/TSGB/RFB/PSM/PCB/NFERKBYE/NCC3810B/NPE/KBLBDE/IOAC/GL1C8R/FP/ESB/DP/CNV1/CNCR/SVFN8/2LD/16M/K4E/GEN1/OVRM/ES_P8/QWCC/EVTRGB/LED14P/X7E5_S80B |
| 431AR4BOL08 | SMT MB AL191 GH51G GN20 E5 Q 8G CB B3L | VGAR/SypwC8/TS_USB/TPM/TSGB/RFB/PSM/PCB/NFERKBYE/NCC3810B/NPE/KBLBDE/IOAC/GL1C8R/FP/ESB/DP/CNV1/CNCR/SVFN8/2LD/16M/K4E/GEN1/OVRM/ES_P8/QWCC/EVTRGB/LED14P/X7E5_S80B |
| 431AR4BOL51 | SMT MB AL191 GH73G GN20 E5 P 8G CC BRL | VGAR/SypwC8/TS_USB/TPM/TSGB/RFB/PSM/PCB/NFERKBYE/NCC3810B/NPE/KBLBDE/IOAC/GL1C8R/FP/ESB/DP/CNV1/CNCR/SVFN8/2LD/16M/K4E/GEN1/OVRM/ES_P8/QWCC/EVTRGB/LED14P/X7E5_S80B |
| 431AR4BOL54 | SMT MB AL191 GH73G GN20 E5 P 8G CB B3L | VGAR/SypwC8/TS_USB/TPM/TSGB/RFB/PSM/PCB/NFERKBYE/NCC3810B/NPE/KBLBDE/IOAC/GL1C8R/FP/ESB/DP/CNV1/CNCR/SVFN8/2LD/16M/K4E/GEN1/OVRM/ES_P8/QWCC/EVTRGB/LED14P/X7E5_S80B |

| Item (X46) | BOM Structure |
|---------------------|---------------|
| EMI requirement | EMI@ V |
| EMI require reserve | XEMI@ V |
| ESD requirement | ESD@ V |
| ESD require reserve | XESD@ V |
| FP ESD requirement | FPESD@ V |
| X4EAR3BOL01 (w/FP) | X4E@ V |
| | X4EFP@ V |

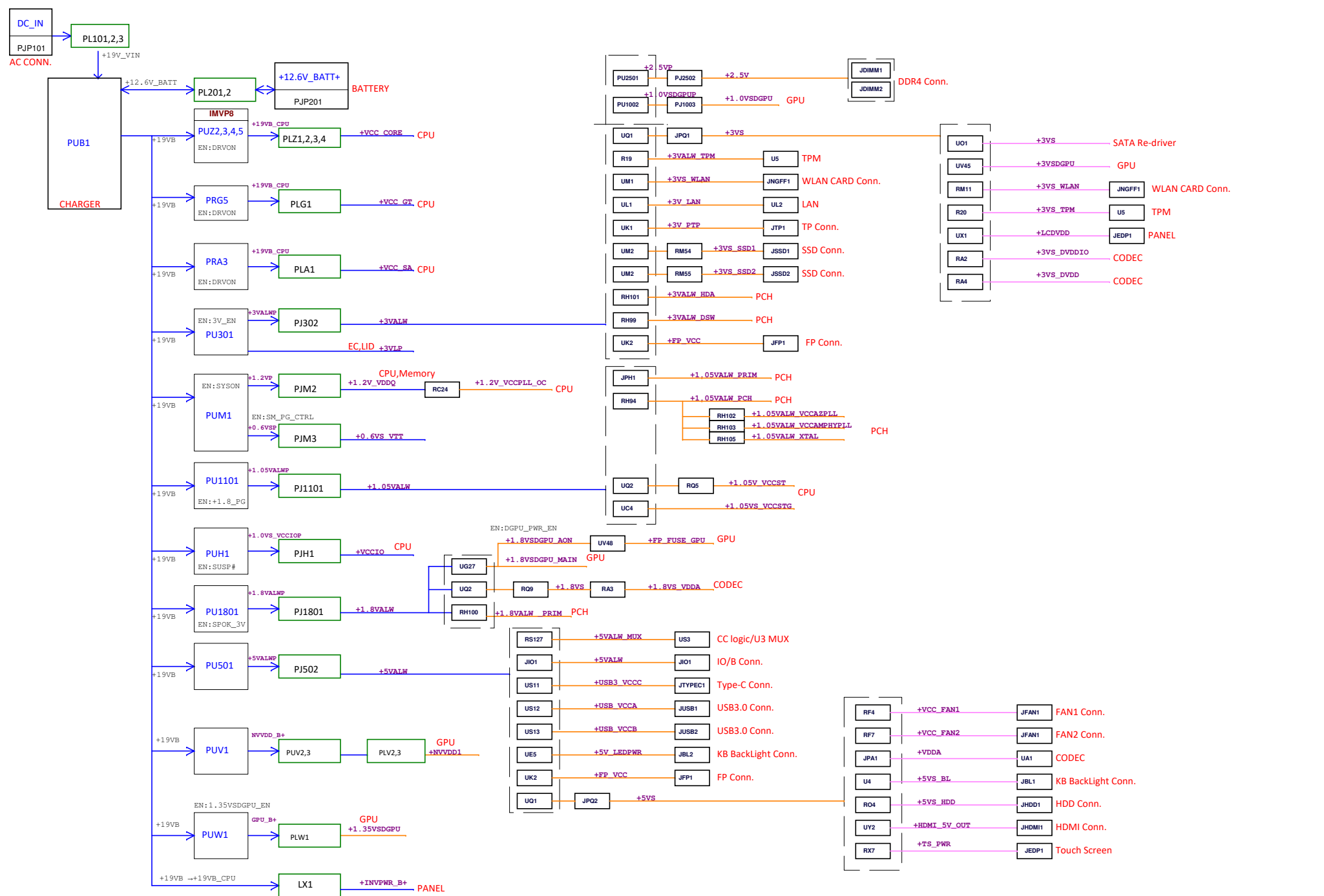
435P4RBOL01(I/O/B)
 X4EP4RBOL01(I/O/B)

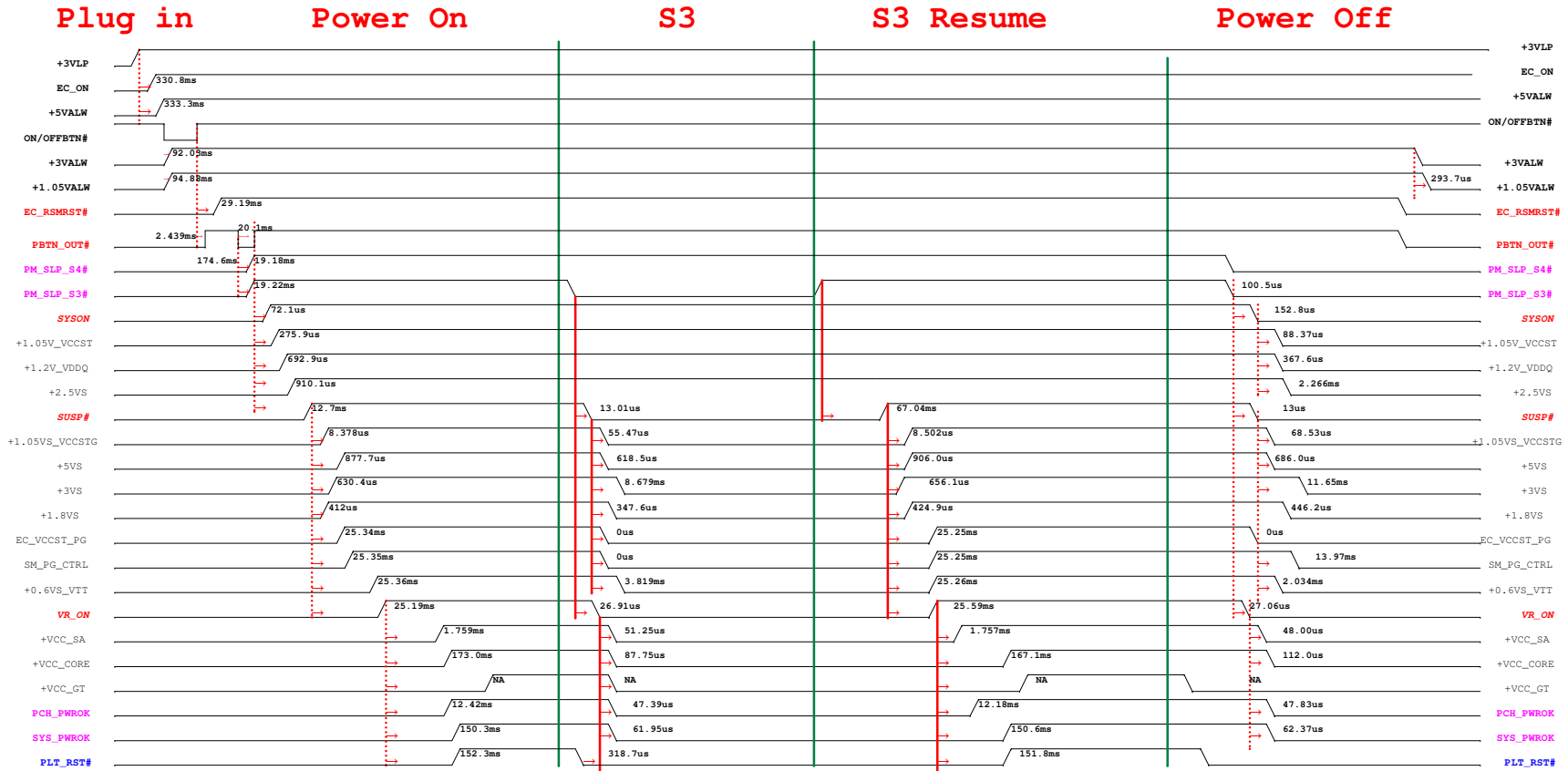
| Item (X76) | BOM Structure |
|------------------|-------------------|
| VRAM-Hynix(6G) | HYN_E3@/X76_HYN@ |
| VRAM-SAMSUNG(6G) | SAM_E3@/X76_SAM@ |
| VRAM-Hynix(8G) | HYN_E57@/X76_HYN@ |
| VRAM-SAMSUNG(8G) | SAM_E57@/X76_SAM@ |

| | | |
|--------------------------|------------|-------|
| X76869BOLA3 - HYNIX 6G | X76E3_HYN@ | E3 |
| X76869BOLA4 - SAMSUNG 6G | X76E3_SAM@ | |
| X76869BOLA5 - HYNIX 8G | X76E5_HYN@ | E5/E7 |
| X76869BOLA6 - SAMSUNG 8G | X76E5_SAM@ | |
| X76869BOLA9 - ON OVRM | X76_ON@ | OVRM |
| X76869BOLAA - uPI OVRM | X76_UPI@ | |

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