## Compal Confidential MB Schematic Document FH51M LA-J871P

**Rev:1.0** 

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	C		D			E				_



	Vcc 3	3.3V +/- 5		Table for AD	) channol				*PCB	Version			SIGNA	L SLP_S3# S
	Ra 1	100K +/- 1			Charmer				ney	board type	-		S0 (Full ON)	HIGH
	Board ID	Rb	Vmin Vt	yp Vmax	EC AD			Board ID	PCB	Revision			S3 (Suspend to RAM	) LOW
	0	0	0.0	00 V 0.300 V	0x00 - 0x13			0	50 F	Rev0.1			S4 (Suspend to Dis	k) LOW
	1 1	12K +/- 1%	0.347 V 0.3	45 V 0.360 V	0x14 - 0x1E	SD0	34120280	1	50 F	Rev0.2			S5 (Soft OFF)	LOW
	2	15K +/- 1%	0.423 V 0.4	30 V 0.438 V	0x1F - 0x25	SDO	34150280	2	50 F	Rev0.3				
	3	20K +/- 1%	0.541 V 0.5	50 V 0.559 V	0x26 - 0x30	SD0	34200280	3	50 F	Rev1.0			Power Plane	Description
	4 4	27K +/- 1%	0.691 V 0.7	02 V 0.713 V	0x31 - 0x32	SD0	34270280	4	50 1	Rev0.2+RGB			+RICVCC	RIC Battery Po
1	5	33K +/- 1%	0.807 V 0.8	19 V 0.831 V	0x3B - 0x45	SD0	34330280	5	50 1	Rev0.3+RGB	_		+19V_VIN	Adapter power
	6 4	43K +/- 1%	0.978 V 0.9	92 V 1.006 V	0x46 - 0x54	SDO	34430280	6	50 1	Rev1.0+RGB	_		+12.6V_BAI1	Battery power
	7 5	56K +/- 1%	1.169 V 1.1	85 V 1.200 V	0x55 - 0x64	SDO	34560280	7	60 F	Rev0.1	_		+19VB	AC or battery p
	8	75K +/- 1%	1.398 V 1.4	14 V 1.430 V	0x65 - 0x76	SDO	34750280	8	60 F	Rev0.2	_		+3VLP	+19VB to +3VLI
	9 1	100K +/- 1	% 1.634 V 1.6	50 V 1.667 V	0x77 - 0x87	SD0	34100380	9	60 F	Rev0.3			+5VALW	+5V Always pov
	10	130K +/- 1	* 1.849 V 1.8	65 V 1.881 V	0x88 - 0x96	SD0	34130380	10	60 1	lev1.0	_		+3VALW	System +3VALV
	11	160K + / - 1	* 2.015 V 2.0	31 V 2.046 V	0x97 - 0xA4	SD0	34160380	11	60	Rev0.2+RGB	_		+3VALW_DSW	+3VALW power
	12 4	200K + 7 - 1	* 2.185 V 2.2	00 V 2.215 V	UXA5 - UXAE	SDU	34200380	12	60 1	Revu.3+RGB	_			
	13 4	240K + 7 - 1	8 2.316 V 2.3	29 V 2.343 V		SDU	00001880	13	60 1	Kevi.0+KGB	_		1.05VALW	1 05V Always
	14 4	270K + 7 - 1	6 2.395 V 2.4	08 V 2.421 V	0xB8 - 0xB	SDU	24220280	14			_		+1.03 VALW	PDP4 +1 2V por
	15 -	330K + 7 - 1	6 2.521 V 2.5	33 V 2.544 V		SDU	34330380	15			_		+1.2V_VDDQ	Sustain voltage
	16 4	430K + 7 - 1	* 2.00/ V 2.0	00 V 2.007 V	$0 \times CA = 0 \times D4$	SDO	24560280	17	-		_		+5VS	System +5V nor
	17	750K +/- 1	6 2.791 V 2.0	10 V 2.808 V		SDU	34360380	10			_		+3VS	System +3V po
	18	750K +/- 1	<sup>8</sup> 2.905 V 2.9 2.000 V 2.9	12 V 2.919 V	0xDE - 0xFC	500	0000AL80	10	-				+105VS VCCSTG	+1 05VALW PR
	19	NC	3.000 V 3.0	00 V	UXFI - UXFE	4		19						DDB +0.6VS por
		<u> </u>	D				A	ddress(8bit)					+VCC_COBE	Core voltage for
	BU	5	De	evice	Address(	bit)	Write	Read	d				+VCC GT	Sliced graphics
2	I2C_0 (+3	3VS) 2	XXXXXX (EMR)										+VCCIO	CPU IO ±0 95V5
	I2C_1 (+3	3VS) -	TM-P3393-003 (Touc	h Pad)									+VCC SA	System Agent r
	PCH SM		DIMM1										+1.8VSDGPU AON	+1 8VS power ra
	(+3VS)		DIMM2						$\overline{\mathbf{Z}}$	$\mathbf{i}$			+1.8VSDGPU MAIN	+1.8VS power ra
	()												+NVVDD1	Core voltage fo
													+1.35VSDGPU	+1.35VS power
	PCH SM		N18P-G0/N17P-G0-K	<1 (VGA)	0x9E					()			+1.0VSDGPU	+1.0VS power ra
	(+3VALW		Thermal Sensor (NCT	F7718W)	1001_100	xb	1001_100	01b   1001_1	000b				+1.8VALW	System +1.8VA
	ÈC_SMB	3_́СК2 🗋	Thermal Sensor (G78	31)	1001_10	xb	1001_101	1b 1001_1	010b					
*	(+3VS)	1	PCH		0x90					~				
	FC SMB		BQ24780 (Charger IC	C)	0x12									
	(+3VLP)		BATTERY PACK		0x16						$\mathcal{O}$		Note : ON* means that this	power plane is
														· ·
	EC SMB	зскз 📙	LED driver		0xC0							$\sim$		
	(+3VALW	/)												
			KC3810		0xC0							/		
3	Th		DOM Of the state	Them (1142) /	WTC) DOM OF		·	_						
	Linner (X4	1) / A/O)	BOM Structure		TE UEDO	ictur'e	43 Leve		Descrip	ot i on	BOM Structure			
	Connoctor		CONNA		12_U28@	a 17								
	Connector					w v	431AMBBC	DLO2 FH51N	1 PG61Q	6 4G	PCB@/H82@/SATANRD@/CMLi5@	/CMLPCH@/VGA@/N18P@/VG	AG610/TS_USB0/NONTS_I2C0/I	P@/IOAC@/CNV
	PCB	Pecerued)		eDP-15 IZC	15_120@	57								
		eserved)	HEJO	For Acer IOAC	040	V 17								
	H82 CPU(RE		H82@ 17	No Acer IOAC	NIOAC@	v								
	CEL ISOS CO		CELISOS@	Intel CNVi		37								_
		-0	CELIEQ	FOR LIADT DT mod		v								
ΗI			CELIDE CELIDE	FOR LIART debug										
	CFL US PUH		CI LE CHIQ3@	Extend GPIO	0ANI@									
			CMUISOS@		VC2010@									
		DI I	CMLIJUJU	Finger Print	FP@	17								
		DI I	CMLIQOS@	FinerPrint(with DD/		V								
		H	CIVILIEUSUU	THEFTING WITH PBA	Remove KP	V FD@								
	CML IS CPU		CML i5@	KB LED driver										
	CML 17 CPU	, 	CMLi7@	FMR 1 8V	WC18V@		Item (X	(4E)	BOM	Structure	Item (X76)	BOM Structure		
	CML i9 CPU		CITELY @	FMR 3 3V	WC33V@		EMI require	ement	EMI@	V	OVRM-uPI	uPL X76@		
4	CML PCH	,	CMIPCH@	Thermal sensor	TMS@	17	EMI require	e reserve	XEMI@	v	OVRM-ON	ON X76@	X76869BOL01 - MIC	RON
	CIVIL FULL		CALL CITE	TDM non	TDM@	v	Elvir require	e reserve	FEDR	77		V765AM@	X/6869B0L02 - SAM	ISUNG
				I PIVI I I I I					E 31 1000		A 10 MARCH 10 MARC			OTTOM

ATE	AL SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+v	+VS		
(Full ON)	HIGH	HIGH	HIGH	ON	ON	ON		
(Suspend to RAM	1) LOW	HIGH	HIGH	ON	ON	OFF		
(Suspend to Dis	sk) LOW	LOW	HIGH	ON	OFF	OFF		
(Soft OFF)	LOW	LOW	LOW	ON	OFF	OFF		
wer Plane	Description				S0	S3	S4	S5
тсусс	RTC Battery P	ower			ON	ON	ON	ON
9V_VIN	Adapter powe	r supply			N/A	N/A	N/A	N/A
2.6V_BATT	Battery powe	r supply			N/A	N/A	N/A	N/A
9VB	AC or battery	power rail	for power o	ircuit.	N/A	N/A	N/A	N/A
/LP	+19VB to +3V	P power r	ail for susp	end power	· ON	ON	ON	ON
VALW	+5V Always p	ower rail			ON	ON	ON	ON
VALW	System +3VA	W always	on power r	ail	ON	ON	ON	ON*
VALW_DSW	+3VALW powe	r for PCH	DSW rails		ON	ON	ON	ON
05VALW	+1.05V Always	s power rai	ON	ON	ON	ON		
2V_VDDQ	DDR4 +1.2V p	ower rail	ON	ON	OFF	OFF		
05V_VCCST	Sustain voltag	e for proce	es ON	ON	OFF	OFF		
/S	System +5V p	ower rail	ON	OFF	OFF	OFF		
/S	System +3V p	ower rail	ON	OFF	OFF	OFF		
05VS_VCCSTG	+1.05VALW_P	RIM Gated	ON	OFF	OFF	OFF		
6VS_VTT	DDR +0.6VS p	ower rail fo	ON	OFF	OFF	OFF		
CC_CORE	Core voltage f	or CPU	ON	OFF	OFF	OFF		
CC_GT	Sliced graphic	s power ra	ON	OFF	OFF	OFF		
CCIO	CPU IO +0.95\	/S power ra	ON	OFF	OFF	OFF		
CC_SA	System Agent	System Agent power rail						OFF
8VSDGPU_AON	+1.8VS power	rail for GP	ON	OFF	OFF	OFF		
8VSDGPU_MAIN	+1.8VS power	rail for GPI	ON	OFF	OFF	OFF		
VVDD1	Core voltage f	or VGA (m	ON	OFF	OFF	OFF		
35VSDGPU	+1.35VS powe	r rail for G	PU		ON	OFF	OFF	OFF
OVSDGPU	+1.0VS power	rail for GP	U		ON	OFF	OFF	OFF
8VALW	ON	ON	ON	ON*				
te : ON* means that thi	s power plane is	ON only v	vith AC pow	ver availabl	e, otherwis	e it is Of	F.	

E

100				45 Level	Description	BOIVI Structure	e		
Unpop	@	eDP-TS USB	TS_USB@						
Connector	CONN@	eDP-TS USB	NONTS_I2C@ V	/31AMBBOL02	EH51M PG61OS /G	PCB@/H82@/SATANRD@/CM	Li5@/CMLPCH@/VGA@/N18P@/VG	AG610/TS_USB0/NONTS_I2C0/DP0/IOAC0/CNV	I@/FP@/PBA@/KBLED@/LED14P@/WC18V@/TMS/@TPM@
PCB	PCB@ V	eDP-TS I2C	TS_I2C@	431AIVIDBOL02	111510110010340				
UMA Only(Reserved)	UMA@	mDP	DP@ V						
H62 CPU(Reserved)	H62@	For Acer IOAC	IOAC@ V						
H82 CPU(POP)	H82@ V	No Acer IOAC	NIOAC@						
CFL i5QS CPU	CFLi5QS@	Intel CNVi	CNVI@ V						
CFL i5 CPU	CFLi5@	FOR UART BT module	UART_BT@						
CFL QS PCH	CFLPCHQS@	FOR UART debug	UART@						
		Extend GPIO	KC3810@						
CML i5QS CPU	CMLi5QS@								
CML i7QS CPU	CMLi7QS@	Finger Print	FP@ V						
CML i9QS CPU	CMLi9QS@	FinerPrint(with PBA)	PBA@ V						
CML QS PCH			Remove KBLED@						
CML i5 CPU	CMLi5@	KB LED driver	LED14P@						
CML i7 CPU	CMLi7@	EMR 1.8V	WC18V@	Item (X4E)	BOM Structure	Item (X76)	BOM Structure		
CML i9 CPU		EMR 3.3V	WC33V@	EMI requirement	EMI@ V	OVRM-uPI	uPI_X76@	X76869BOL01 - MICRON	
CML PCH	CMLPCH@	Thermal sensor	TMS@ V	EMI require rese	rve XEMI@	OVRM-ON	ON_X76@	X76869BOL02 - SAMSUNG	
		ТРМ рор	TPM@	ESD requirement	ESD@ V	VRAM-SAMSUNG	X76SAM@	X76869BOL03 - ON OVRM	
		TPM non-pop	NTPM@ V	ESD require rese	rve XESD@	VRAM-MICRON	X76MIC@	X76869BOL04 - UPI OVRM	
dGPU circuit	VGA@ V	SSD3 pop	SSD3@	FP ESD requirem	ent FPESD@ V			-	
N17P GPU	N17P@					Security Classification	Compal	Secret Data	Compal Electronics. Inc.
N18P GPU	N18P@ V					Issued Date	2019/09/20 [	Deciphered Date 2020/09/20	Title Notes List
N18P-G61	VGAG61@	PVT	PVT@	X4EAMBBOL01	PG6162 FOR EE	THIS SHEET OF ENGINEERING DR	AWING IS THE PROPRIETARY PROPERTY OF C	OMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL	Size Document Number Rev
N18P-G62 MP2	VGAG62@	PVT W/RGB	PVTRGB@	X4EP4MBOL01	PG6162 IO FOR EE	DEPARTMENT EXCEPT AS AUTHO MAY BE USED BY OR DISCLOSED	RIZED BY COMPAL ELECTRONICS, INC. NEITHEF TO ANY THIRD PARTY WITHOUT PRIOR WRITTE	R THIS SHEET NOR THE INFORMATION IT CONTAINS IN CONSENT OF COMPAL ELECTRONICS, INC.	Custom FH51M M/B LA-J871P 1.0



 Power Map

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AC mode



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