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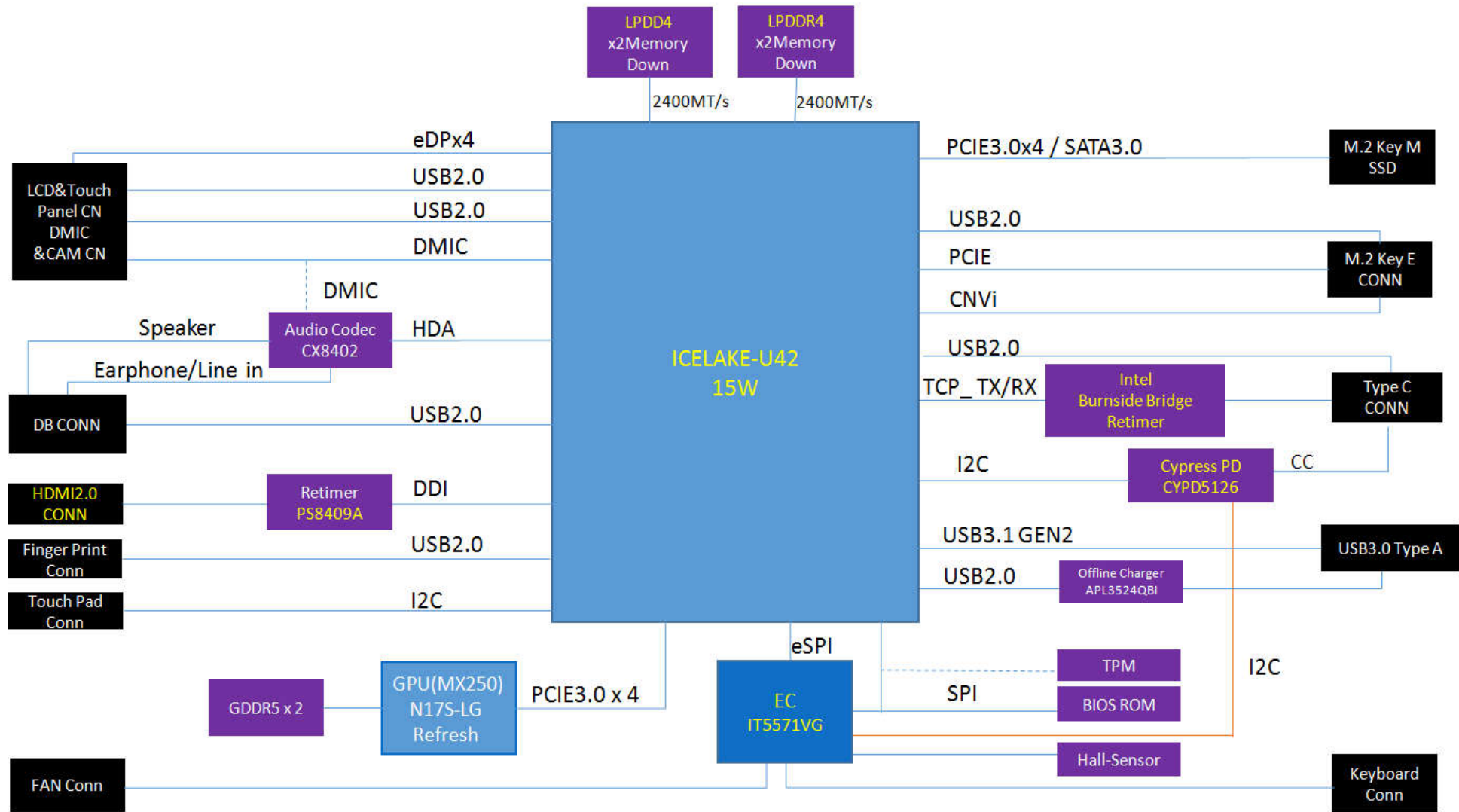
NB8511/12_M/B Schematics Document

Intel ICL Lake U-Processor with LPDDR4

REV1.0

2019-02-01

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Reviewer	Nelosl.Hai & Nemo.Jiang	Page name: Cover page		
Approver	Lobo_Fan	Size: A4	Project Name: NB8511	REV: V1.0
		Date: Monday, July 15, 2019	Sheet: 1	of 72



MEM ID

HW_ID3	HW_ID2	HW_ID1	HW_ID0	Description	Total
0	0	0	0	SAMSUNG LPDDR4 3733 1GB K4F8E304HB-MGCJ LF+HF D20	4GB
0	0	0	1	HYNIX LPDDR4 3733 1GB H9HCNNN8KUMLHR-NME LF+HF DDP	4GB
0	0	1	0	MICRON LPDDR4 4266 2GB MT53E512M32D2NP-046 WT:E LF+HF Z11N	8GB
0	1	0	0	HYNIX LPDDR4 3733 2GB H9HCNNNBPUMLHR-NME LF+HF DE	8GB
0	1	0	0		16GB
1	0	0	0	HYNIX LPDDR4X 4266 4GB H9HCNNNCFMALHR-NEE LF+HF QDP	
				4x 16Gb(reserve)	

GPU ID

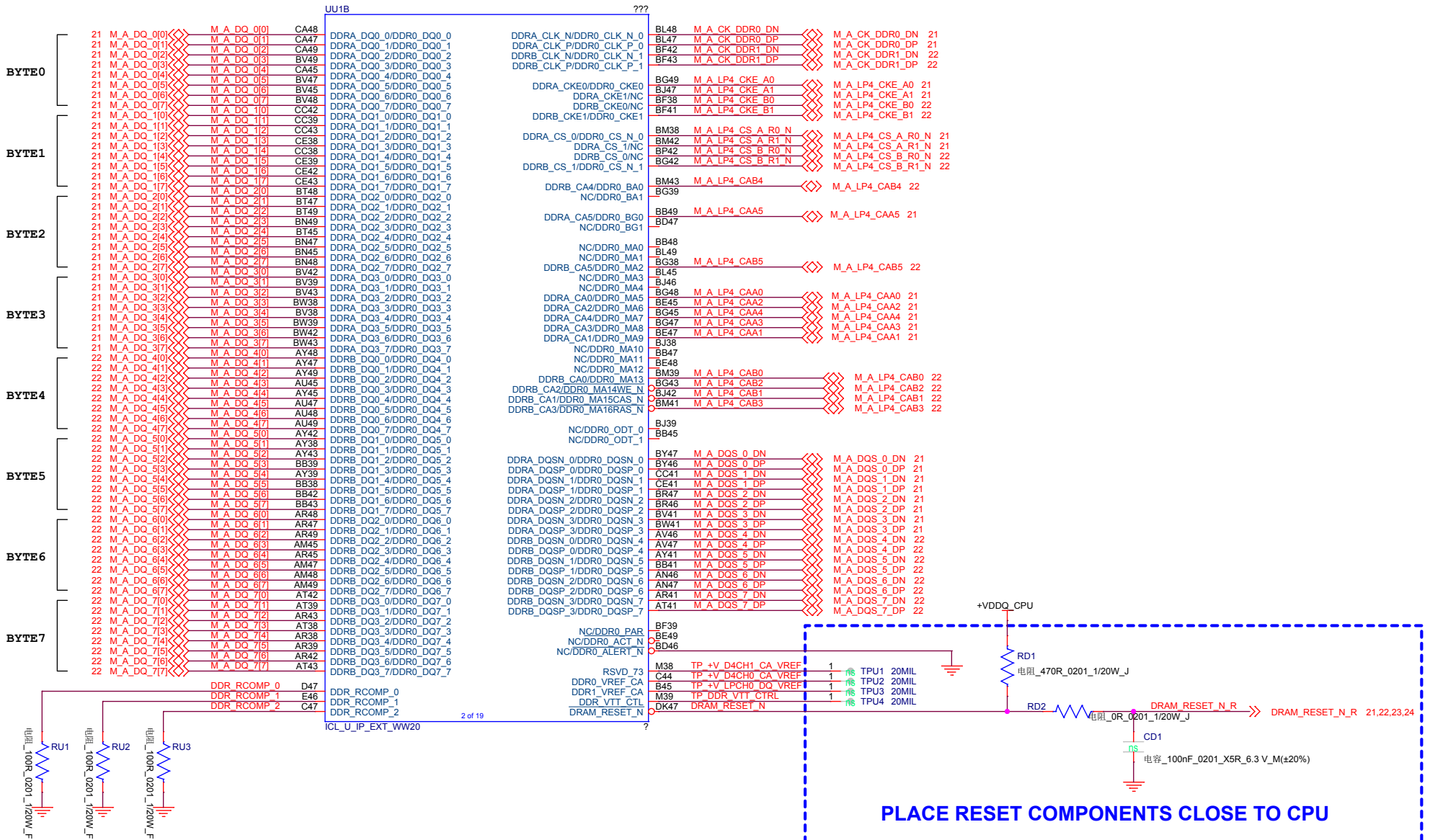
HW_ID5	HW_ID4	Description	
		N17-LG-Refresh	N17-LG
0	0	NC	NC
1	0	Mount	
1	1		Mount

KB BL ID

HW_ID6	Description
0	No keyboard Backlight
1	Keyboard Backlight

Reserve ID

HW_ID7	Description
0	Reserve
1	Reserve



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Page name: ICL-U42(CHA)	
Size: A4	Project Name: NB8511
Date: Monday, July 15, 2019	REV: V1.0
Sheet: 5	of 72



ICL_U_IP_EXT_VW20 3 of 19