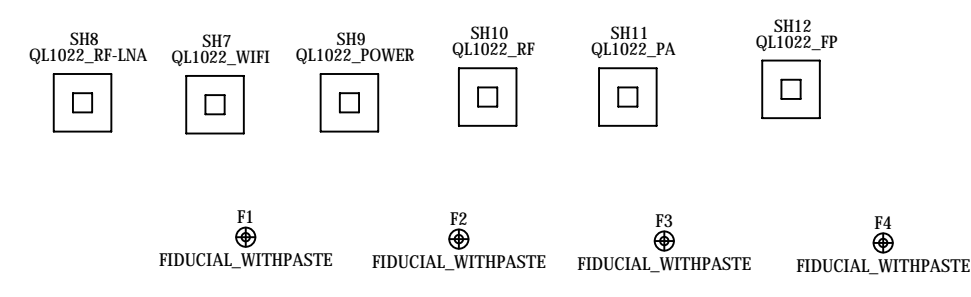
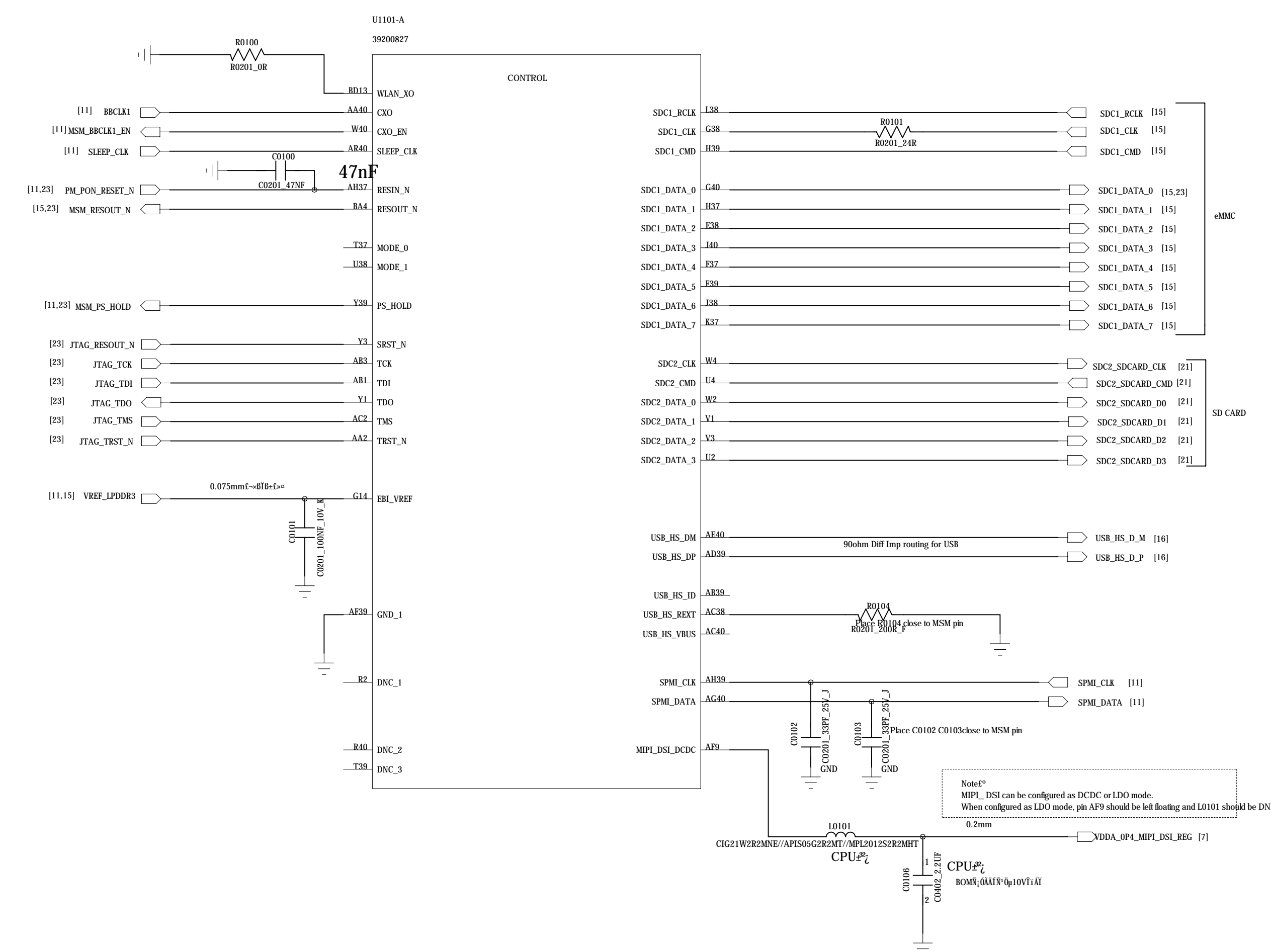
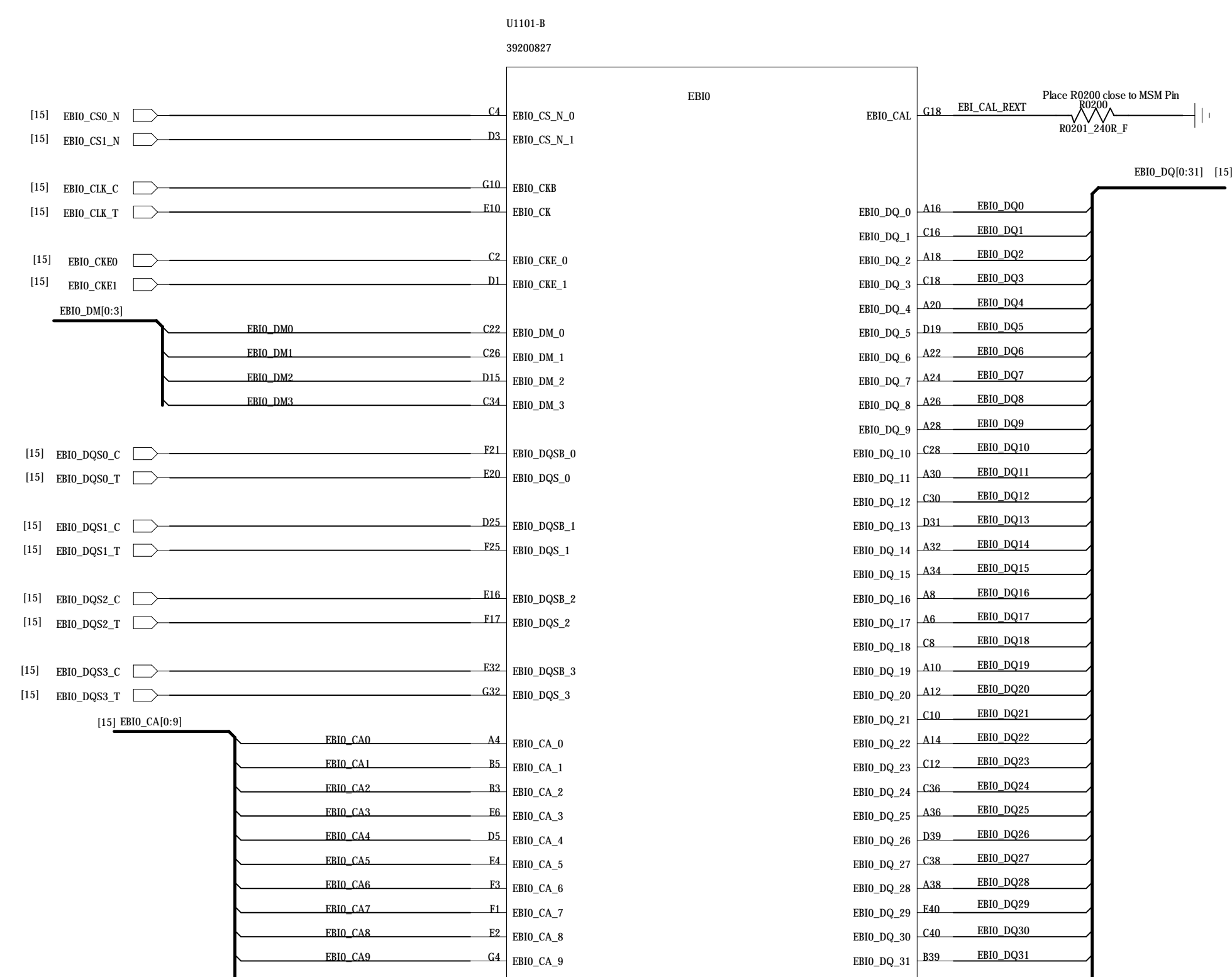


REVISION RECORD			
ITER	ECO NO.	APPROVAL	DATE



COMPANY: HUAWEI		TITLE: 01_MSM8917_CONTROL	
DESIGN: GUWEINA	DATE: <Drawn Date>	CODE: <Code>	SIZE: E
CHECKED: <Checked By>	DATE: <Checked Date>	DRAWING NO: <Drawing Number>	
QUALITY CONTROL: <QC By>	DATE: <QC Date>	BY: V1.0	
RELEASED: <Released By>	DATE: 8/22/14	SCALE: <Scale>	SHEET #/ 31

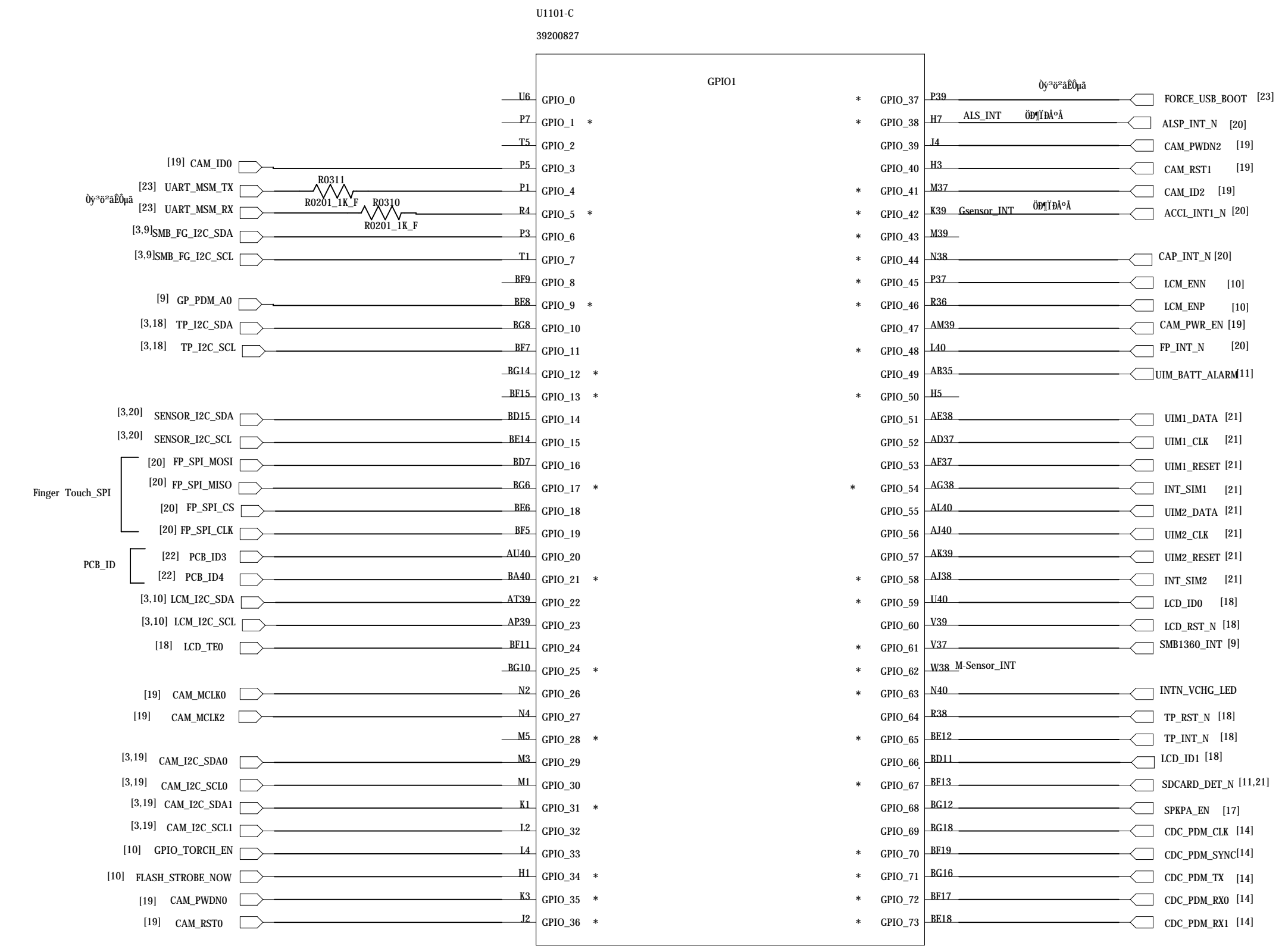
REVISION RECORD			
ITR	ECO NO.	APPROVED	DATE



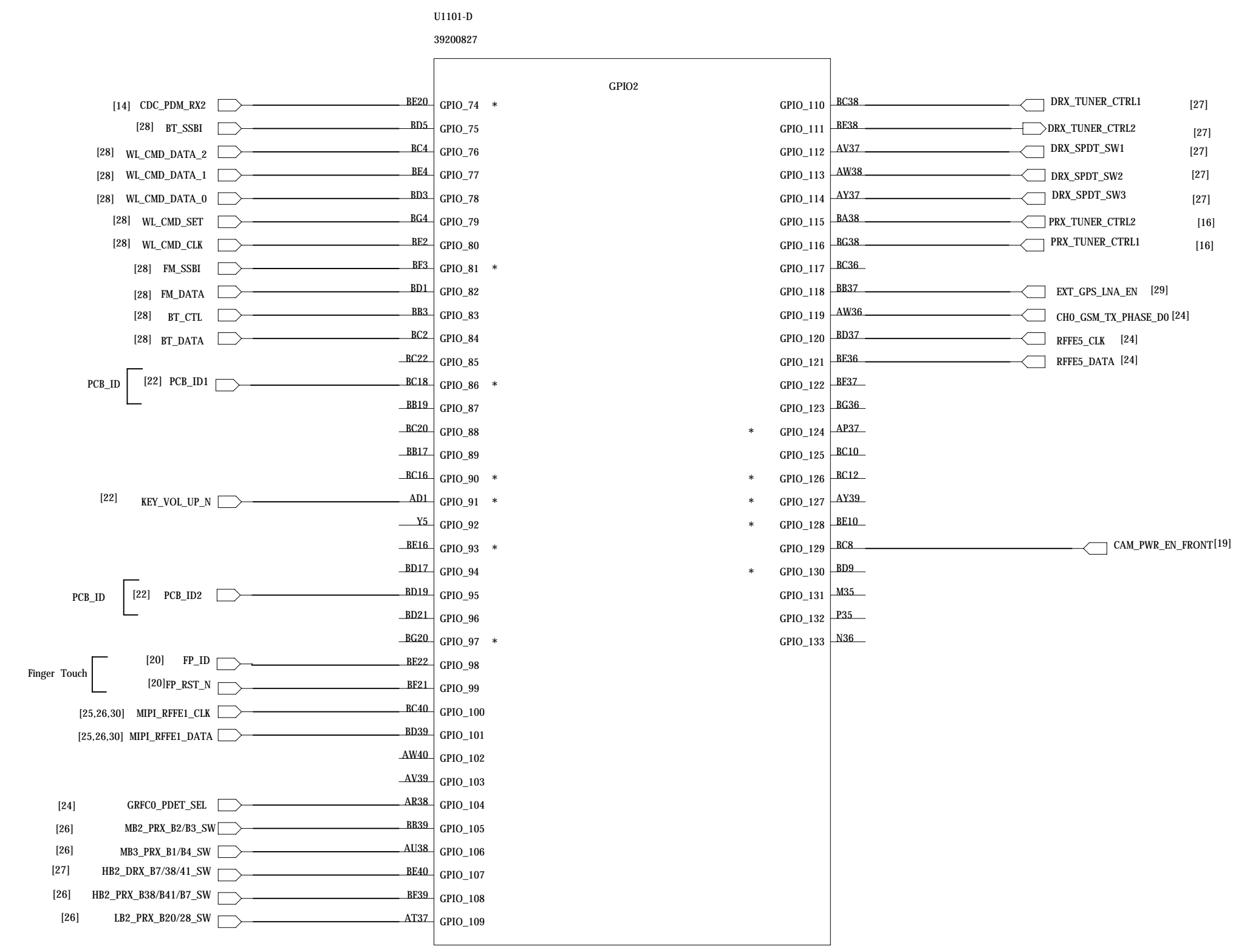
DRAWN: GUIWENA		DATE: <Drawn Date>		COMPANY: HUAWEI	
CHECKED: <Checked By>		DATE: <Checked Date>		TITLE: 02_MSM8917:_EBI	
QUALITY CONTROL: <QC By>		DATE: <QC Date>		CODE: <Code>	SIZE: E <Drawing Number> V1.0
RELEASED: <Released By>		DATE: 9/22/14		SCALE: <Scale>	SHEET 8/ 31

REVISION RECORD			
REV	REV NO	APPROVED	DATE

Note: Asterisks (\*) indicate modem power management (MPM) wake-up pins



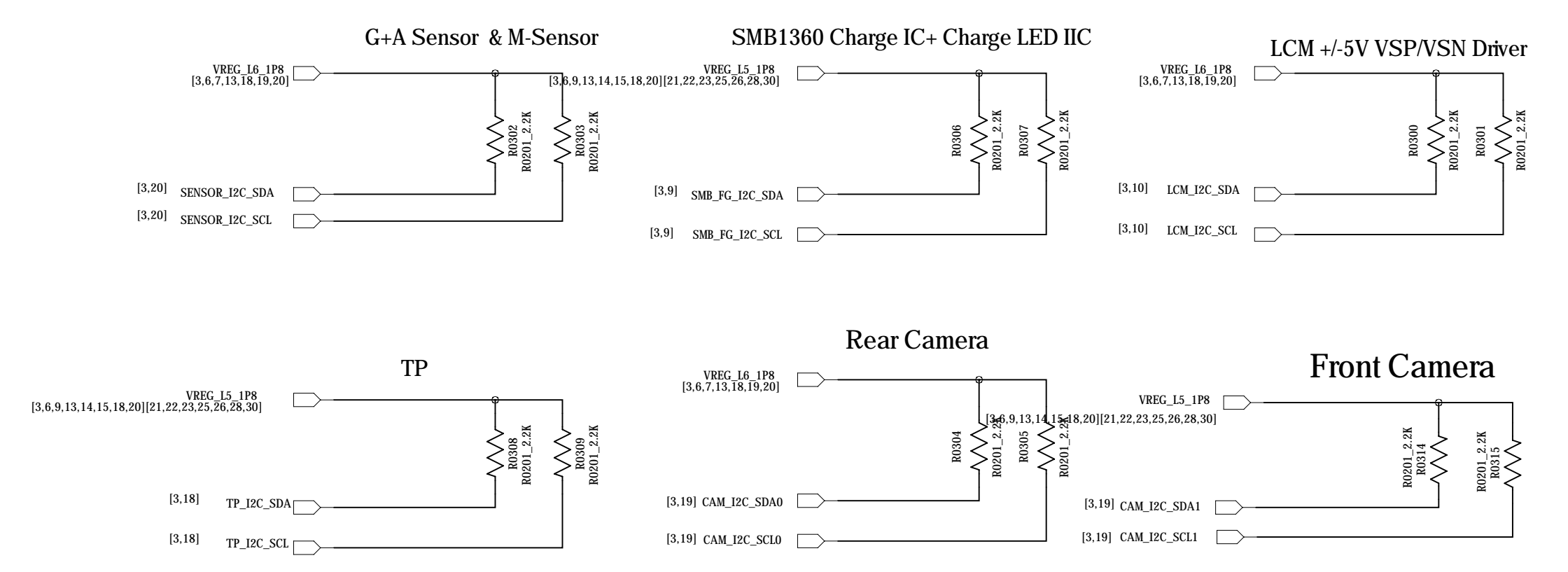
New  
Manual developer use  
[Board config] [ Apps, Boot, FROM, ROM ]



Note: Do not have pull-ups on the following GPIOs unless intend for boot or secure- boot related configurations:  
GPIO\_91, 107, 109

I2C PULL-UP RESISTORS

Note: Ensure SW sets these GPIOs (Sensor, CTP and Camera I2C bus) to high pull down when the peripherals are powered off to eliminate leakage.



GPIO	BOOT_CONFIG
GPIO_3	BOOT_CONFIG0
GPIO_111	BOOT_CONFIG0
GPIO_88	BOOT_CONFIG2
GPIO_48	BOOT_CONFIG2

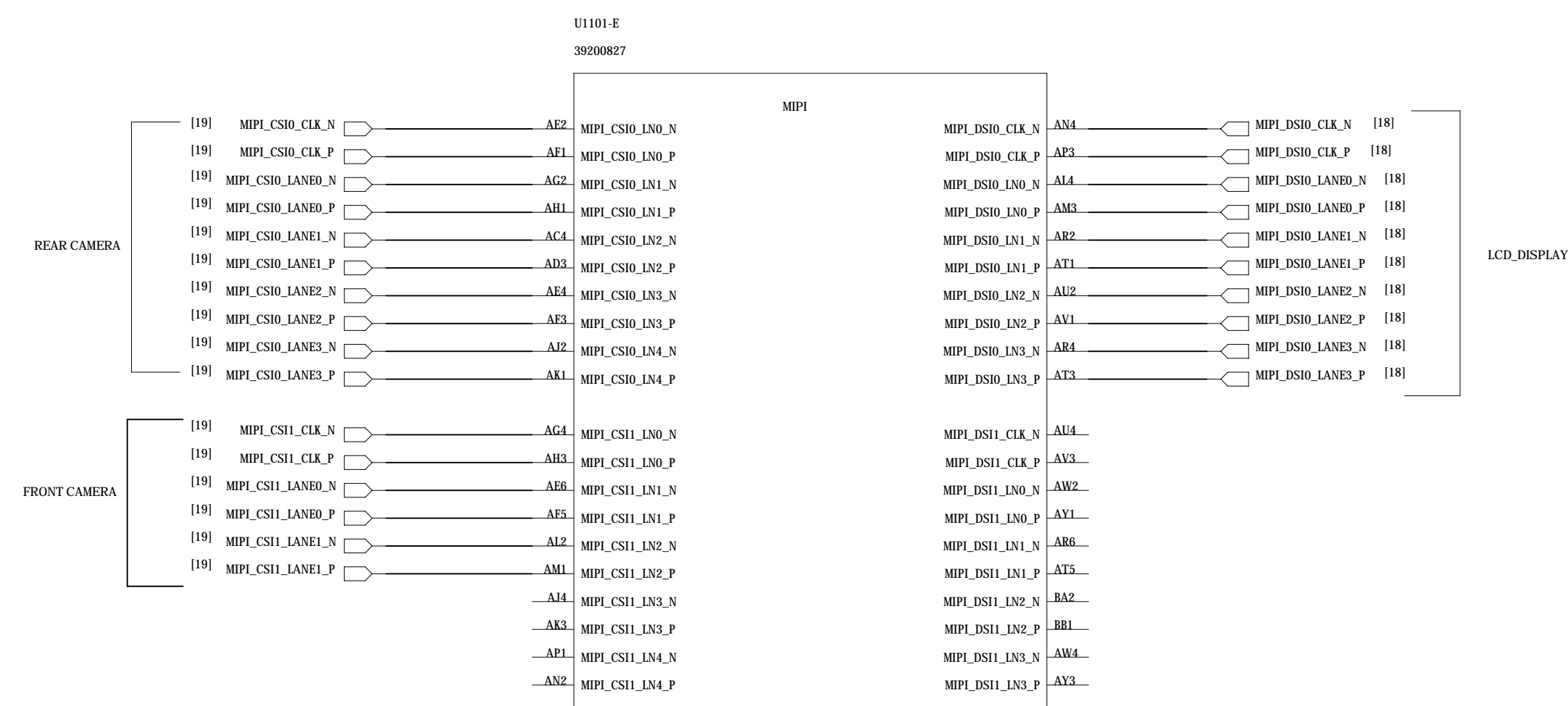
BOOT_CONFIG[3]	BOOT_CONFIG
0x00	SDC1 - SDC2 - USB2.0
0x01	SDC1
0x02	SDC3 - SDC1

Default Boot Config (0x00) is SDC1+MSDC

CONFIGURATION GPIO'S	
GPIO_37	FORCED_USB_BOOT
GPIO_3	WDOG_DISABLE
GPIO_111	FASTBOOT_SELECT(0)
GPIO_112	FASTBOOT_SELECT(1)
GPIO_88	FASTBOOT_SELECT(2)
GPIO_113	APPS_AARCH64_ENABLE
GPIO_63	APPS_BOOT_FROM_ROM
GPIO_114	APPS_PBL_BOOTSPD(0)
GPIO_41	APPS_PBL_BOOTSPD(1)
GPIO_106	PK_HASH_INDEX_SRC
GPIO_8	ALL_USE_SERIAL_NUMBER
GPIO_9	MSA_PK_HASH_IN_FUSE
GPIO_107	MSA_AUTH_EN
GPIO_108	AP_PK_HASH_IN_FUSE
GPIO_109	AP_AUTH_EN
GPIO_91	FORCE_MSA_AUTH_EN
GPIO_92	PBL_DEBUG

COMPANY		HUAWEI	
TITLE			
03_MSM8917-_GPIO			
DESIGN	GUWENA	DATE	<Drawn Date>
CHECKED	<Checked By>	DATE	<Checked Date>
QUANTITY CONTROL	<QC By>	DATE	<QC Date>
RELEASED	<Released By>	DATE	9/22/14
SCALE		<Scale>	SHEET 36 31

REVISION RECORD			
REV	ECO NO.	APPROVED	DATE



Note: If best EMI practices are followed for MIPI CSI/DSI signals, there is no need for common mode choke filters. You may choose to have placeholders for common mode depending upon your design constraints. Extreme care must be taken that no stubs are created by doing so.

DRAWN: GUWEINA		DATE: <Drawn Date>		COMPANY: HUAWEI	
CHECKED: <Checked By>		DATE: <Checked Date>		TITLE: 04_MSM8917:_MIPI	
QUALITY CONTROL: <QC By>		DATE: <QC Date>		CODE: <Code>	SIZE: E
RELEASED: <Released By>		DATE: 9/22/14		SCALE: <Scale>	DRAWING NO: <Drawing Number>
				SHEET: 46	TOTAL: 31

