

Selek 15" Schematic


CFL-H refresh

2019/04/03

REV : A00

DY : None Installed
UMA : UMA only installed
OPS : DISCRTE OPTIMUS installed

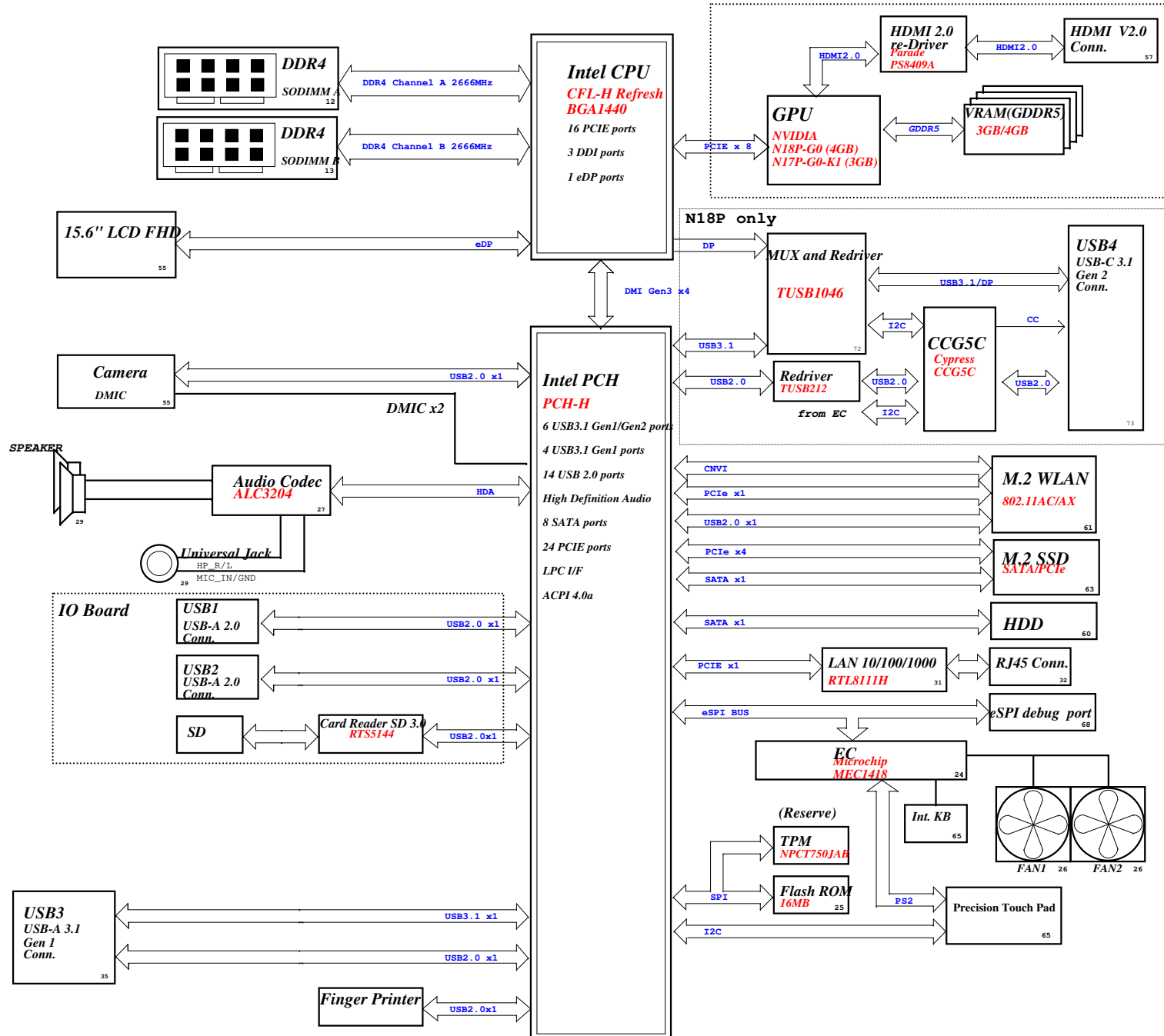
Selek CFLH N17P

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title Cover Page		
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Project Code : 4PD0H7010001
 PCB P/N : 18825-1
 Revision : A00

Selek CFL-H refresh Block Diagram

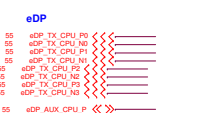
GPU



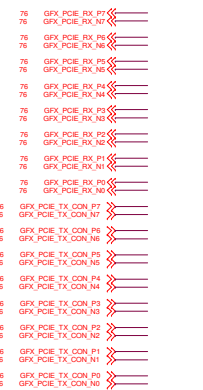
CHARGER		44
ISL88739		
INPUTS	OUTPUTS	
AD+	DCBATOUT	
BT+		
SYSTEM DC/DC		45
TPS51225RUKR-GP		
INPUTS	OUTPUTS	
DCBATOUT	3D3V_PWR 3D3V_S5 5V_PWR 5V_S5	
CPU Core Power		46-50
NCP81208MNTXG		
NCP81382MNTXG x 2		
NCP81382MNTXG (23e)		
NCP81253MNTBG		
INPUTS	OUTPUTS	
DCBATOUT	VCC_CORE	
DCBATOUT	+VCCGT	
DCBATOUT	+VCCGT (23e)	
DCBATOUT+VCCGA		
DDR4 SUS		51
RT8231AGQW-GP		
AP15930KAI-TRG		
INPUTS	OUTPUTS	
DCBATOUT	1D2V_S3 0D5V_S0 2D5V_S3	
CPU VCCPRIM_CORE		1V
1V		
INPUTS	OUTPUTS	
1D0V_S5	+VCCPRIM_CORE	
CPU DCDC-V1D00A		53
AO22262Q1-10-GP-U		
INPUTS	OUTPUTS	
DCBATOUT	1D0V_S5	
LDO-V1D8V		54
AP15930KAI-TRG		
INPUTS	OUTPUTS	
3D3V_S5	1D8V_S5	
5V/3V S0		40
TPS22966DPUR-GP		
INPUTS	OUTPUTS	
3V_S5 3D3V_S5	3V_S0 3D3V_S0	
EOPIO/EDRAM (23e)		40
TPS22961DNVT		
INPUTS	OUTPUTS	
1D0V_S5 1D0V_S5	+V_EDRAM_VR +V_EOPIO_VR	
3D3V VGA		86
AO3419L		
INPUTS	OUTPUTS	
3D3V_S0	3D3V_VGA_S0	
VGA CORE		85
ISL62771HRTZ-GP-U		
INPUTS	OUTPUTS	
DCBATOUT	VGA_CORE	
1D5V_VGA_S0		86
Y8288RAC-GP		
INPUTS	OUTPUTS	
DCBATOUT	1D5V_VGA_S0	

Selek CFL-H N17P

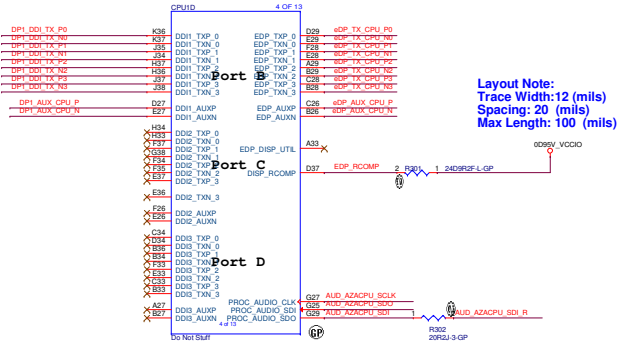
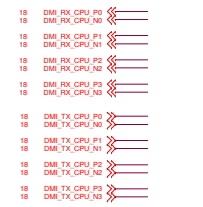
DP to Type-C



PEG



DMI



Do Not Stuff

Layout Note:
Trace Width: 12 (mils)
Spacing: 15 (mils)
Max Length: 400 (mils)



Selak CFL-H N17P

DELL Wistron Corporation
2/F, 8F, 9F, 11F, 12F, 13F, 14F, 15F, 16F, 17F, 18F, 19F, 20F, 21F, 22F, 23F, 24F, 25F, 26F, 27F, 28F, 29F, 30F, 31F, 32F, 33F, 34F, 35F, 36F, 37F, 38F, 39F, 40F, 41F, 42F, 43F, 44F, 45F, 46F, 47F, 48F, 49F, 50F, 51F, 52F, 53F, 54F, 55F, 56F, 57F, 58F, 59F, 60F, 61F, 62F, 63F, 64F, 65F, 66F, 67F, 68F, 69F, 70F, 71F, 72F, 73F, 74F, 75F, 76F, 77F, 78F, 79F, 80F, 81F, 82F, 83F, 84F, 85F, 86F, 87F, 88F, 89F, 90F, 91F, 92F, 93F, 94F, 95F, 96F, 97F, 98F, 99F, 100F

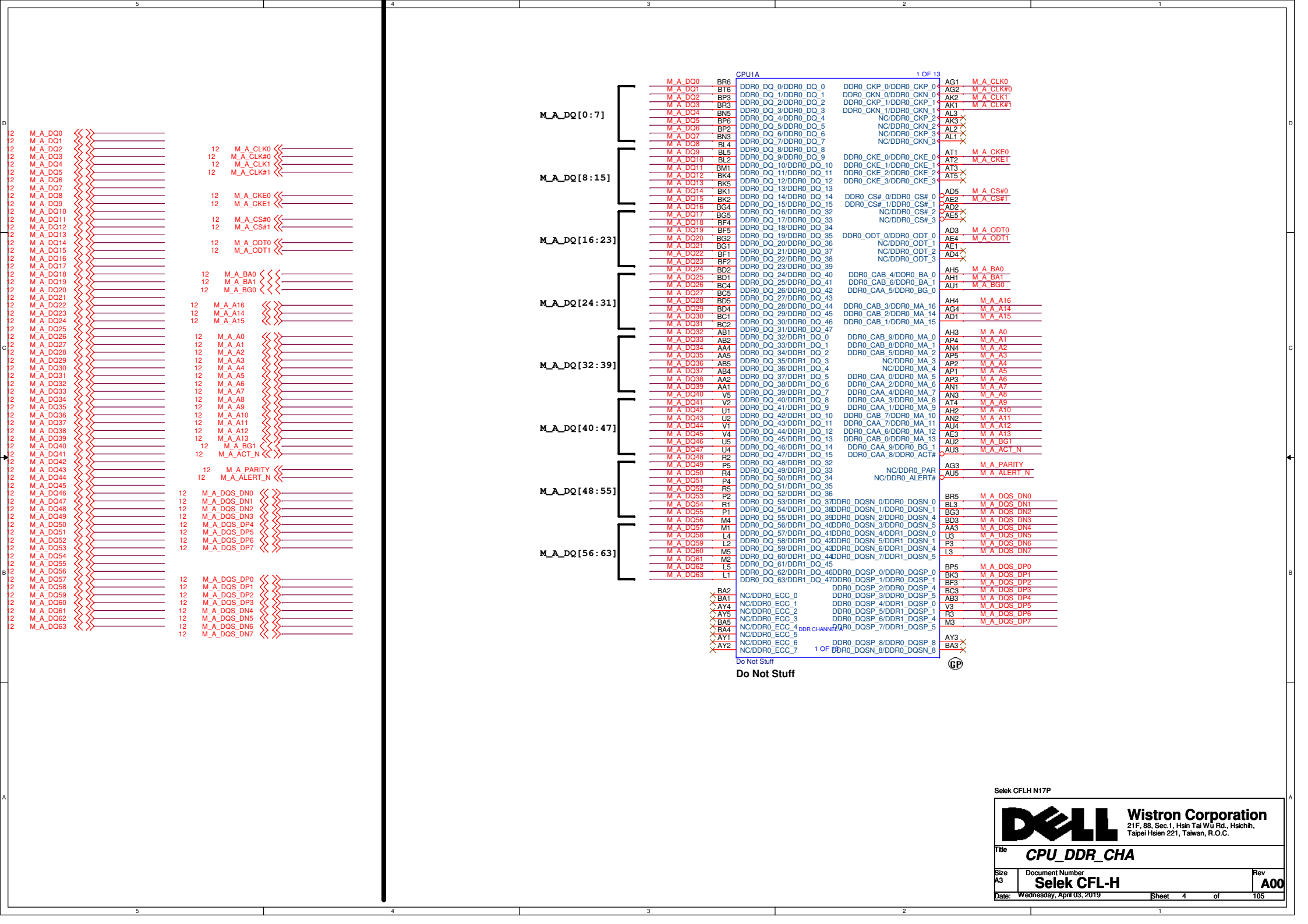
CPU (PCIE/DMI/FDI)

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Date: **Wednesday, 19/11/2014**

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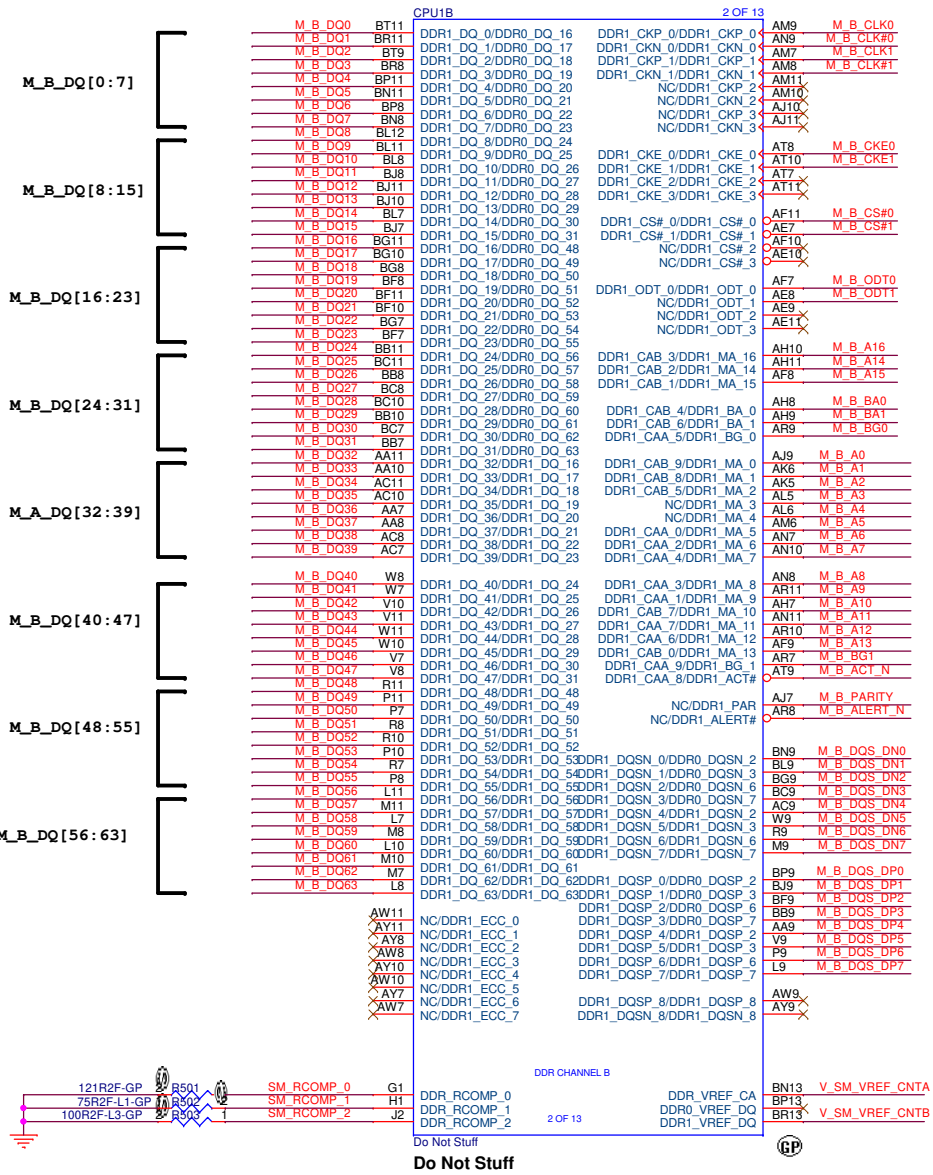
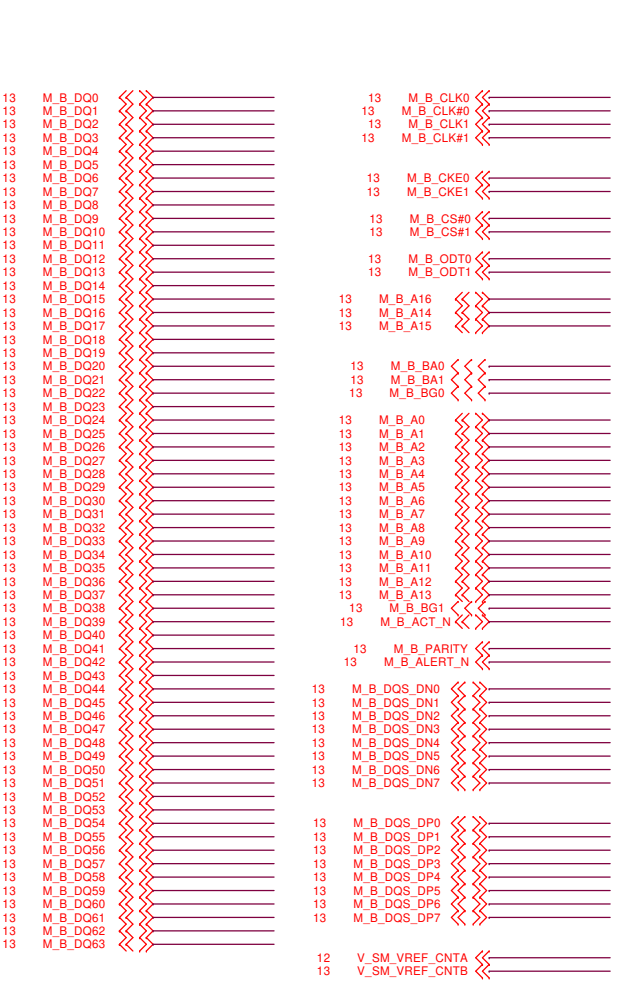


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SSID = CPU



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Title: **CPU_DDR_CHB**

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