Compal LA-H32AP

Herschel 15 GPP53

Schematic Document

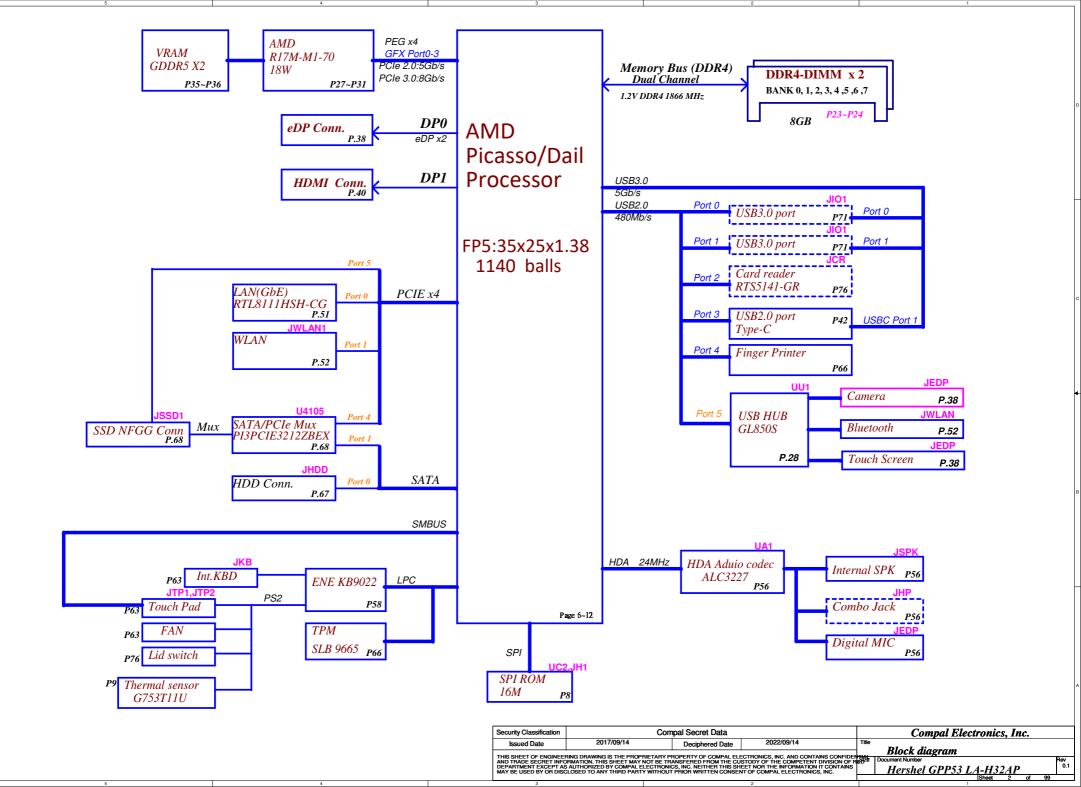
AMD APU Picasso/Dali

AMD GPU R17M-M1-70 (Radeon 530)

2019/11/01

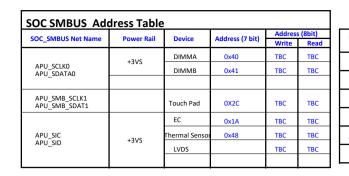
Rev: 0.2

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Power rail	Control (EC)	Source (CPU)
+3V_LID	x	X
VIN	x	X
BATT+	x	X
+19VB	x	x
+VL	x	x
+3VL	x	X
+5VALW	EC_ON	x
+3VALW	EC_ON	x
+3VALW_EC	x	x
+1.2V_VDDQ	PG_+2.5V	PM_SLP_S5#/PM_SLP_S4#
+0.6VS_VTT	SUSP#	
+5VS	SUSP#	PM_SLP_S3#
+3VS	SUSP#	PM_SLP_S3#
+APU_CORE		VR_ON
+APU_GFX		GFX_VR_OFF

@ is NO SMT part (empty)
short@: short pad, don't pop.
@EMI@,@ESD@,@RF@: Reserve, don't pop.
RF@: RF team request, must add.
EMI@: EMI team request, must add.
ESD@: ESD team request, must add.
LVDS@: Support LVDS panel.
eDP@: Support eDP panel.
PX@: GPU BOM config.



	<usb port=""></usb>									
USB3.0 Port	DESTINATION	USB2.0 Port	DESTINATION							
0 - 0	Туре С	0 - 0	USB3 Type A							
0 - 1	Type C	0 - 1	USB3 Type A							
0 - 2	USB3 Type A	0 - 2	Card Reader							
0 - 3	USB3 Type A	0 - 3	Туре С							
1 - 0 T1 only		1 - 0	FPR							
		1-1	USB HUB							

EC SMBUS Address Table

EC SMBUS Port	Power Rail	Device	Address (7 bit)	Address (8bit)	
EC_SIVIBUS POIL	Power Kall Device		Address (7 bit)	Write	Read
SMBUS Port 1	+3VALW_EC	BAT	TBC	твс	твс
SIVIBUS FUIL I		CHGR	TBC	твс	твс
	+3VS	APU Int sensor	0x4C	0x98	0x99
SMBUS Port 2		DGPU Int sensor	0x41	0x82	0x83
		APU External Thermal Sensor	0x48	0x90	0x91

<pupe, sat<="" th=""><th colspan="9">(FUI-E, JATA, ULK></th></pupe,>	(FUI-E, JATA, ULK>								
	PCI-E SATA		DESTINATION	CLK					
	0		LAN	CLK1					
	1		WLAN	CLK2					
T1 only	2		Х						
T1 only 3 4			Х						
			PCI-E SSD	CLK3					
	5		PCI-E SSD	CLK3					
T2 SATA only	6	0	HDD						
T2 SATA only	7	1	SSD or ODD						
	GFX		GPU	CLK0					





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PCB PCB_6L@



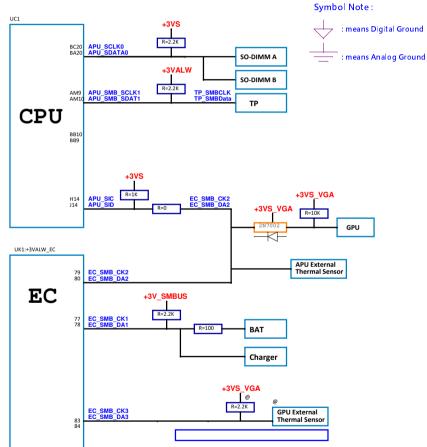
-DOLE SATA OLK



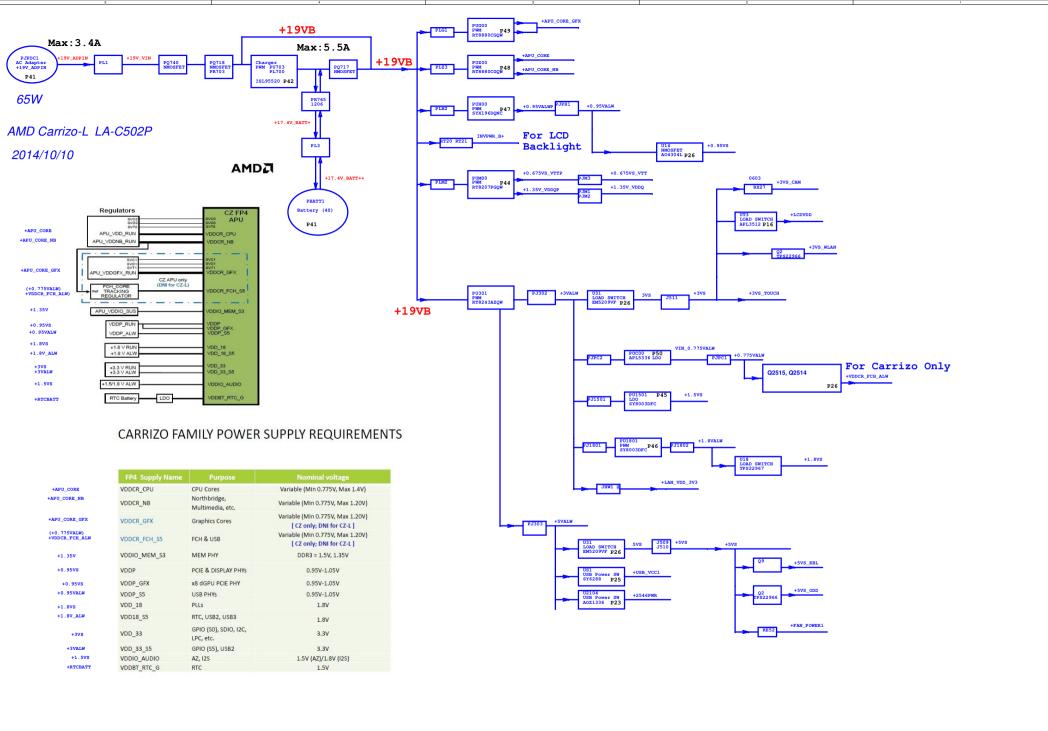
HY2_R1@

HY2_R3@

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