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1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.  
 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.  
 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

# X646 MLB

REV	ECN	DESCRIPTION OF REVISION	CK APPD	DATE
				2016-10-06

SCHEM, X646, MLB


LAST\_MODIFICATION=Thu Oct 6 17:00:29 2016

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DRAWING TITLE: SCHEM, X646, MLB  
 ABBREVIATION: DRAWING  
 LAST\_MODIFIED: Thu Oct 6 17:00:29 2016

SCHEM, X646, MLB

DRAWING TITLE		SCHEM, MLB, X646	
 Apple Inc.	DRAWING NUMBER	051-01506	SIZE
	REVISION	5.19.0	
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BOM Variants

Table with 3 columns: BOM NUMBER, BOM NAME, BOM OPTIONS. Lists various BOM variants for PCBA, MLB, CTO, EL XTA, VRAM, etc.

Bar Code Labels / EEEE #'s

Table with 6 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), CRITICAL, BOM OPTION. Lists bar code labels and their corresponding BOM options.

ALTERNATE PARTS TABLE LOCATED ON CSA 115

J135 SCHEMATIC / PCB #'S

Table with 6 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), CRITICAL, BOM OPTION. Lists schematic and PCB part numbers.

BOM Groups

Table with 2 columns: BOM GROUP, BOM OPTIONS. Lists BOM groups like X646\_COMMON, X646\_PROGPARTS, etc.

CPUs

Table with 6 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), CRITICAL, BOM OPTION. Lists CPU parts.

SOCKET& CONNECTOR

Table with 6 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), CRITICAL, BOM OPTION. Lists socket and connector parts.

ASICs

Table with 6 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), CRITICAL, BOM OPTION. Lists ASIC parts.

Programmable Parts

Table with 6 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), CRITICAL, BOM OPTION. Lists programmable parts like ROM, FLASH, etc.

GPU and VRAM

Table with 6 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), CRITICAL, BOM OPTION. Lists GPU and VRAM parts.

Apple Inc. BOM Configuration metadata including drawing number (051-01506), revision (5.19.0), and page information (2 OF 121).

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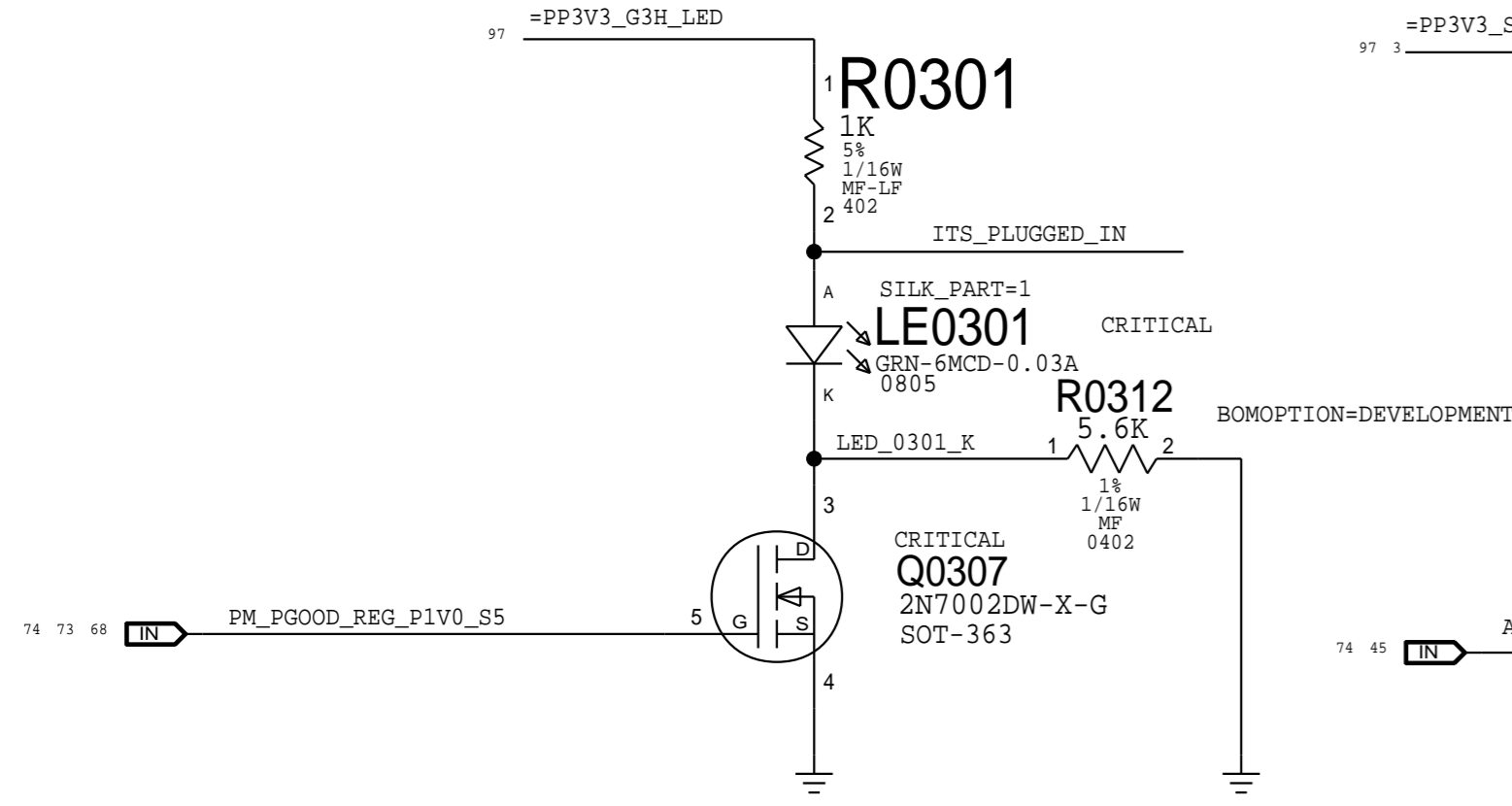
4

3

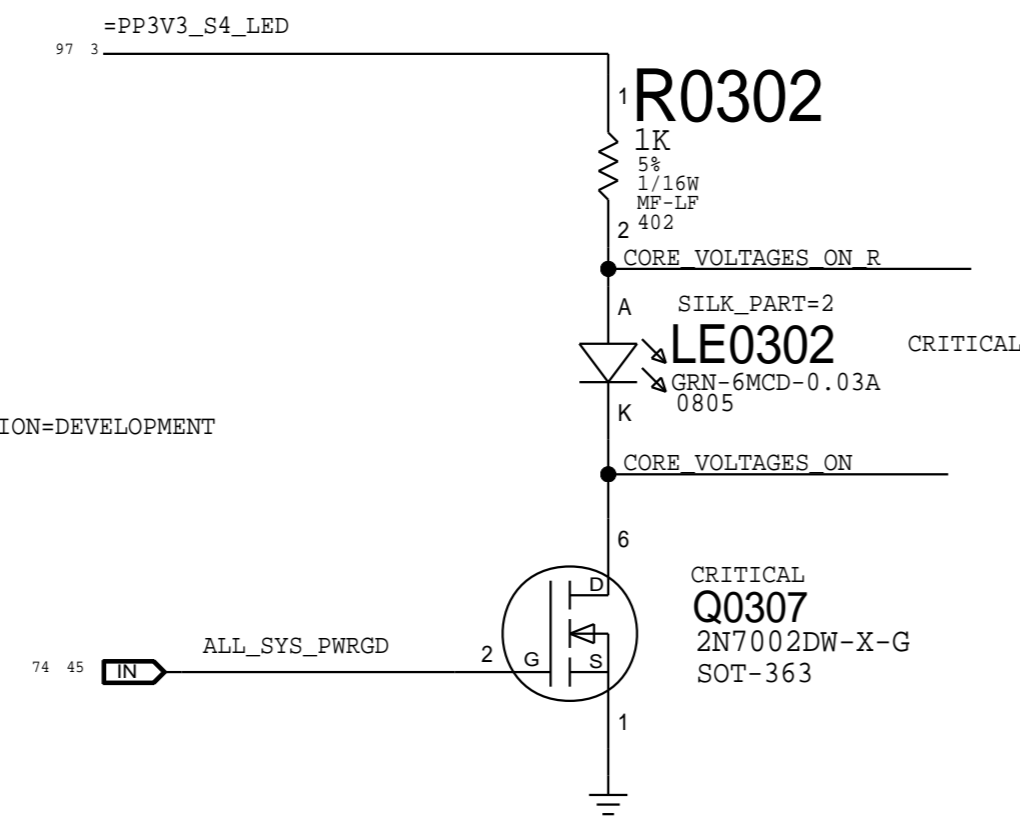
2

1

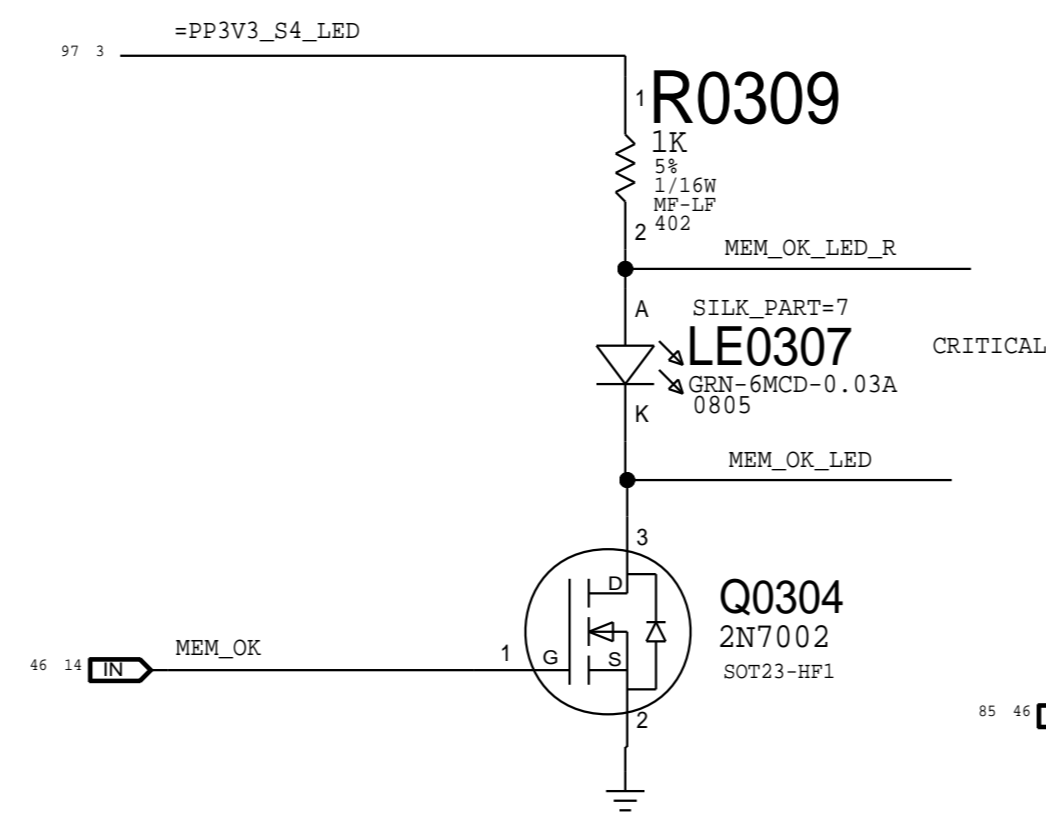
S5 Pwr LED



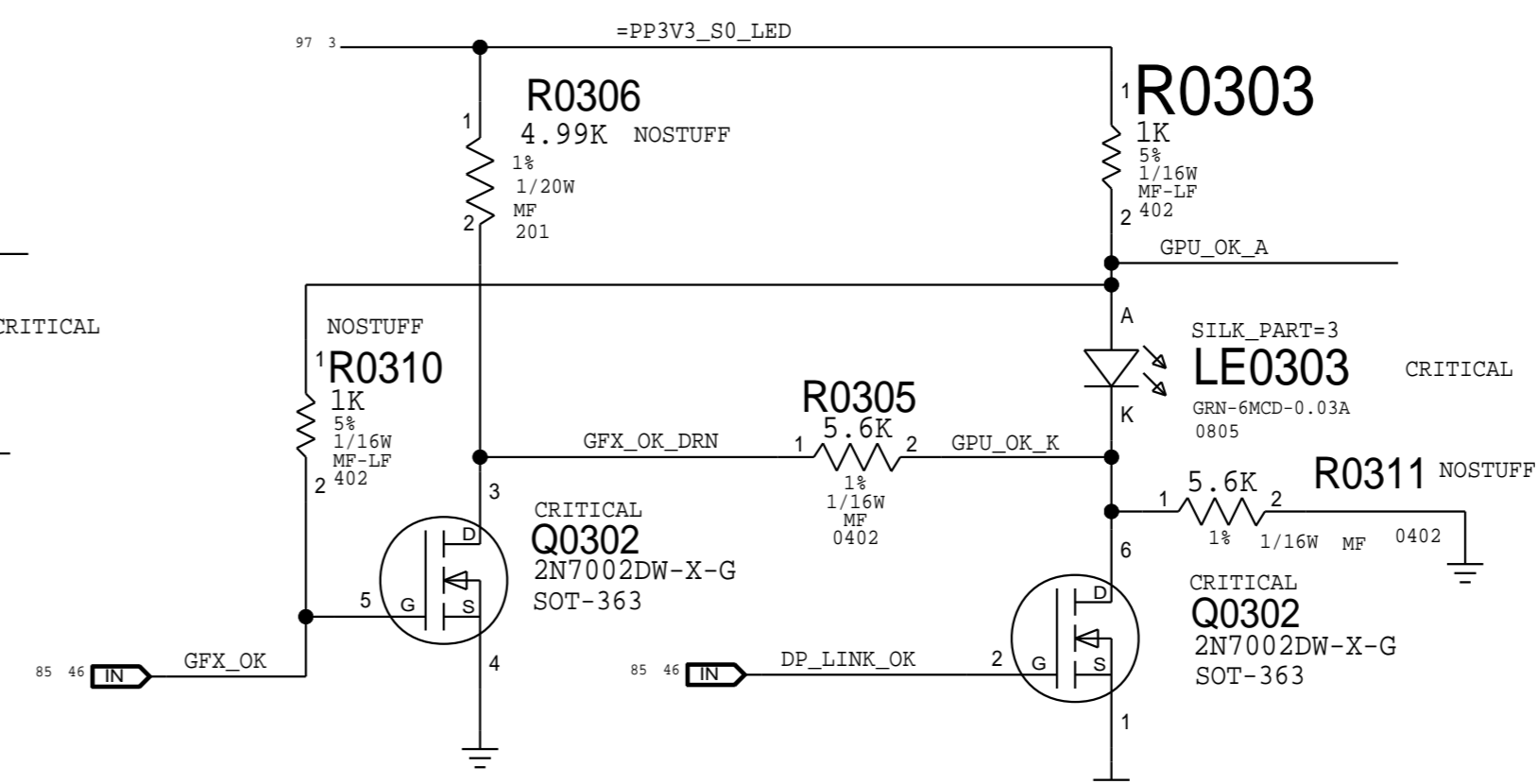
ALL\_SYS\_PWRGD LED



MEM\_OK LED

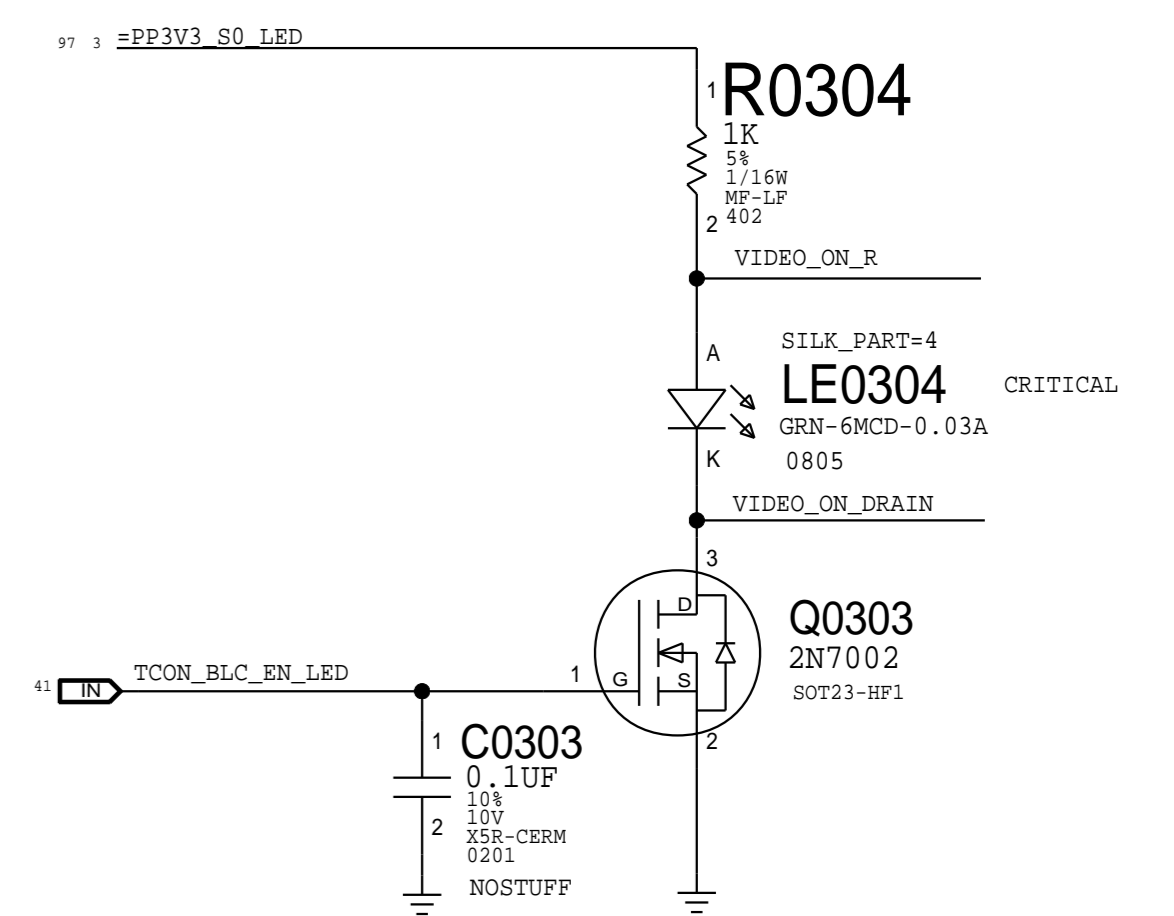


GPU OK LED

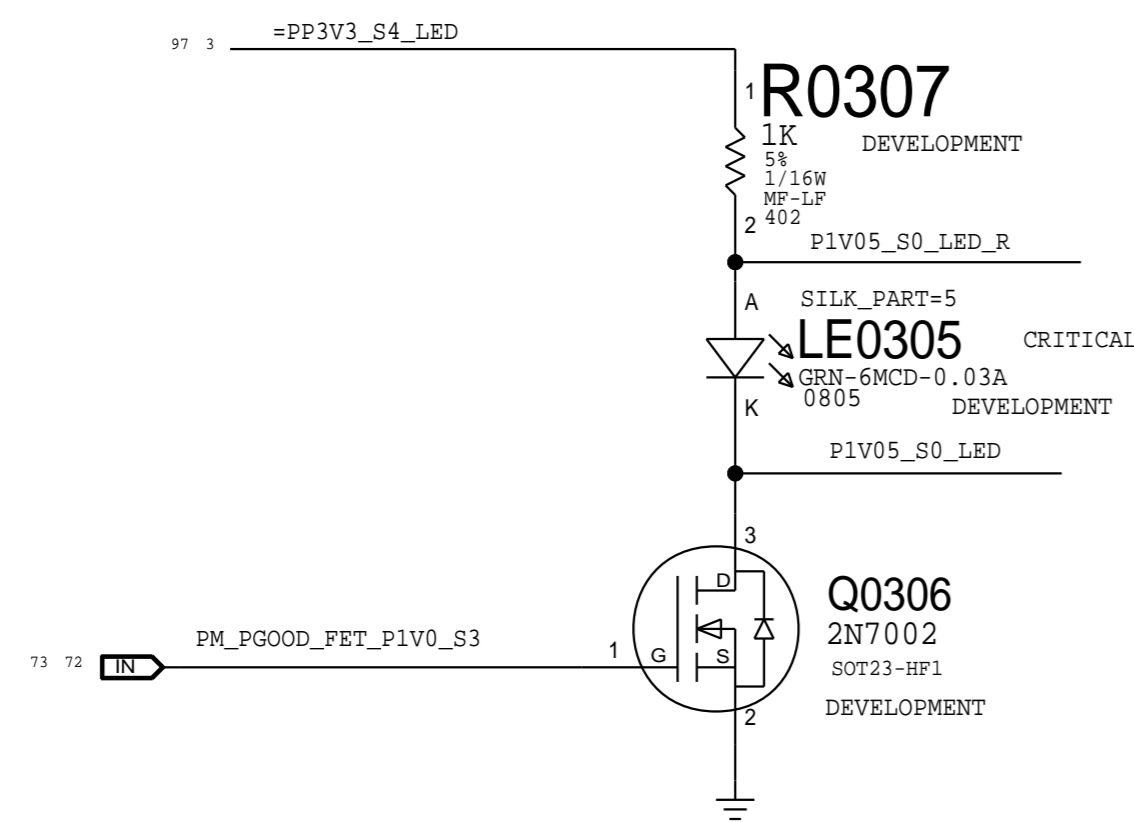


THIS LED IS DRIVEN BY GPU:  
GFX\_OK HIGH LIGHTS THE LED DIMLY  
DP\_LINK\_OK HIGH LIGHTS THE LED FULLY

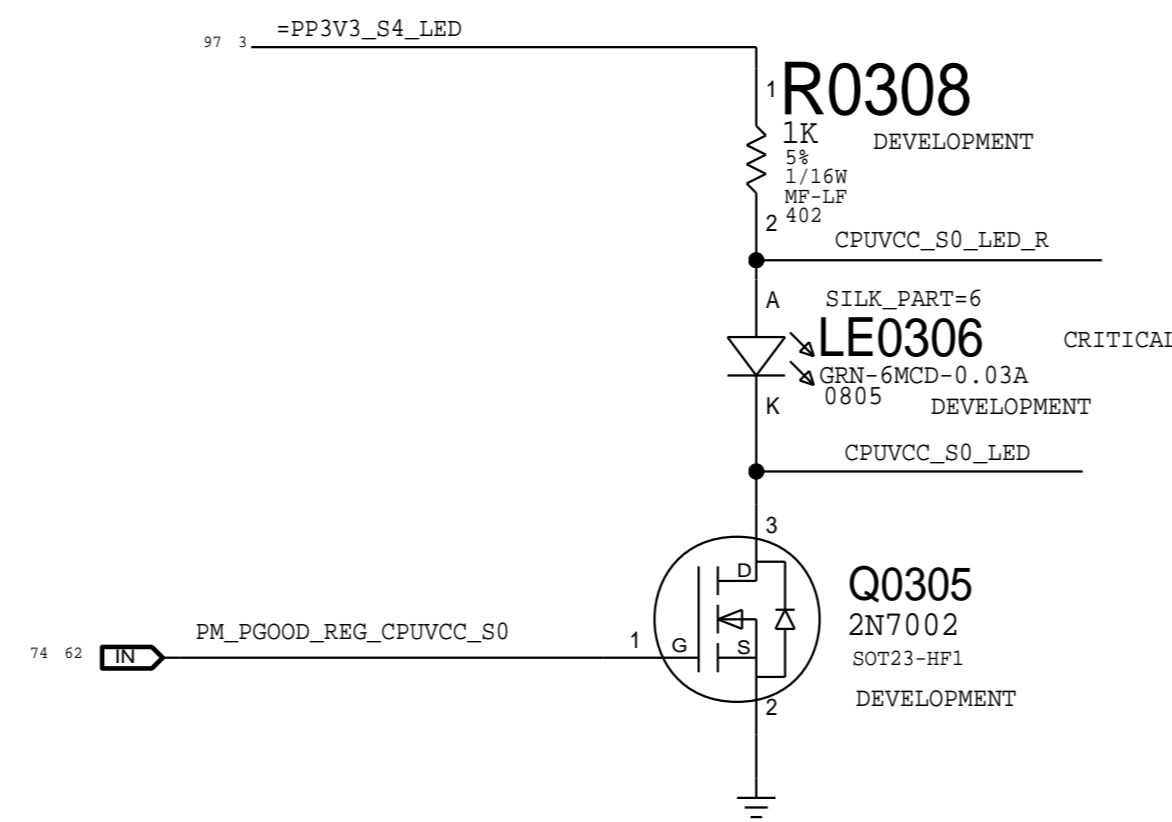
TCON OK LED



P1V0\_S3\_PWRGD LED

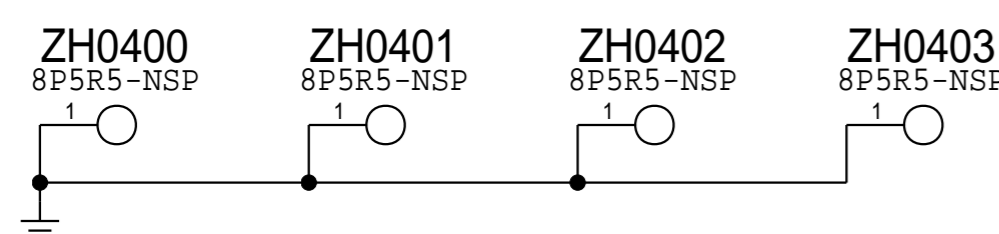


CPUVCC\_S0\_PWRGD LED

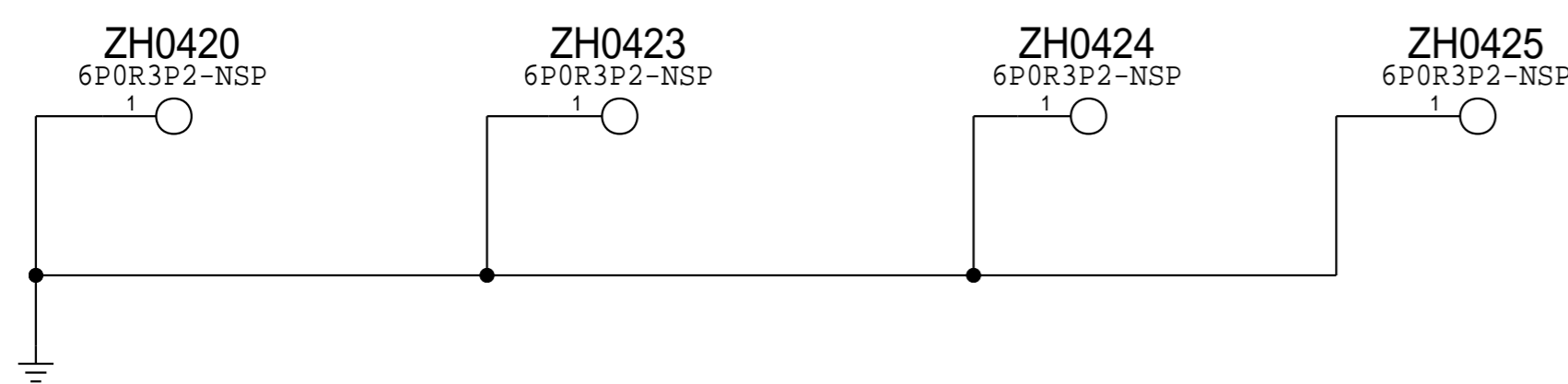


EVMC_MASTER=ANDREW		SYMC_DATE=02/18/2016	
PAGE TITLE			
		DRAWING NUMBER	051-01506
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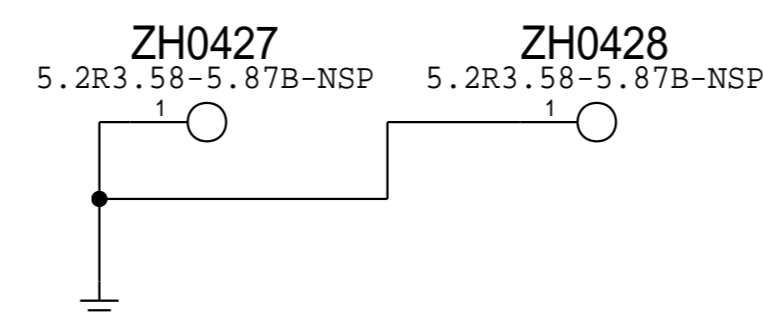
CPU Heatsink  
4MM PLATED HOLES (998-4158)



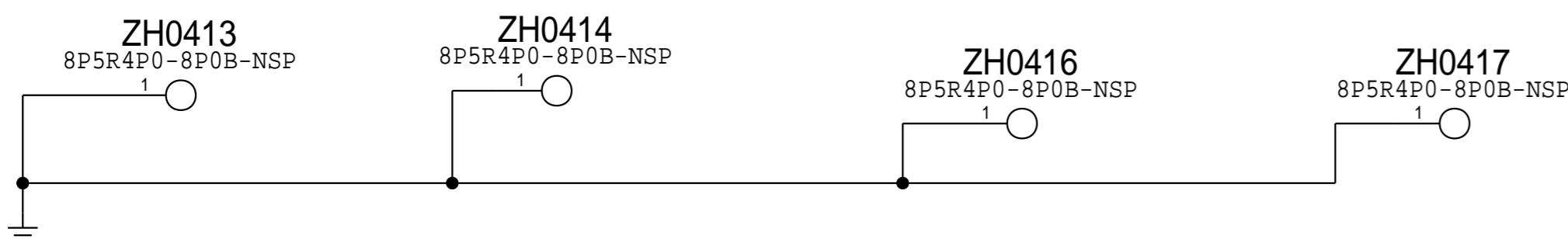
GPU HEATSINK MOUNTING FEATURES  
(998-5013. PLATED HOLE, 3.2MM DIA, 6MM PAD TOP/BOT)



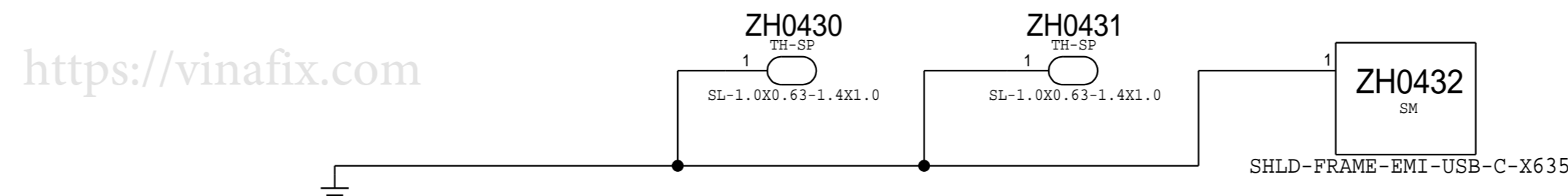
GPU HEATPIPE MOUNTING FEATURES  
998-06191 (PLATED HOLES, 3.58MM INNER DIAMETER)



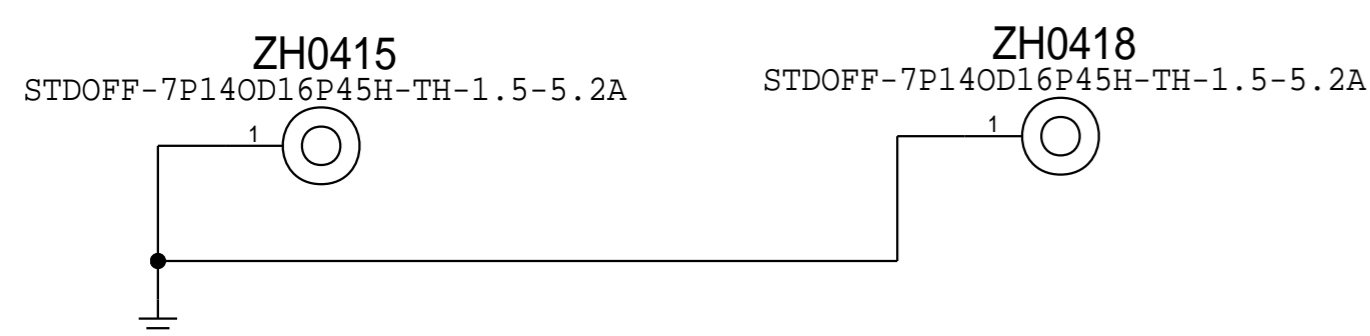
Rear Cover  
998-5014 (PLATED HOLES, 4MM DRILL, 8.5MM TOP, 8MM BOT)



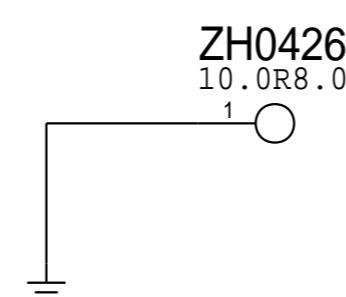
USB-C Alignment Holes and EMI Can



Rear Cover  
860-5674 (PCB STANDOFF)



HEATPIPE MTG HOLES  
998-5527 (PLATED HOLES, 8MM DIA, 10MM PAD)

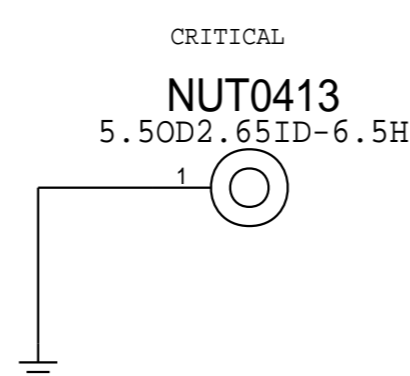


Vinafix.com

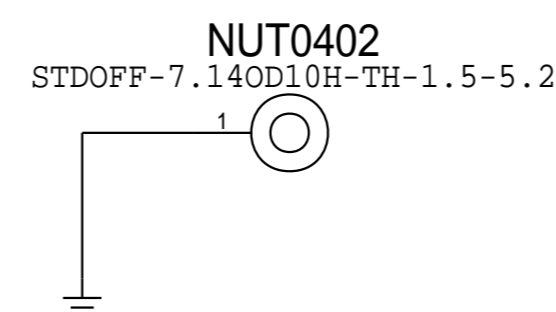
WIRELESS CARD MTG HOLES  
998-06186 (PLATED HOLES, 3.3mm INNER DIAMETER, 5mm Top pad)



SSD STANDOFF  
APN: 860-00314



LOL Boss  
860-5675 (PLATED HOLE, 8.41mm pad Top, 8mm pad Bot)



BOM\_COST\_GROUP=MECHANICALS

PAGE TITLE <b>MECHANICAL: Holes/PD parts</b>		
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D

C

B

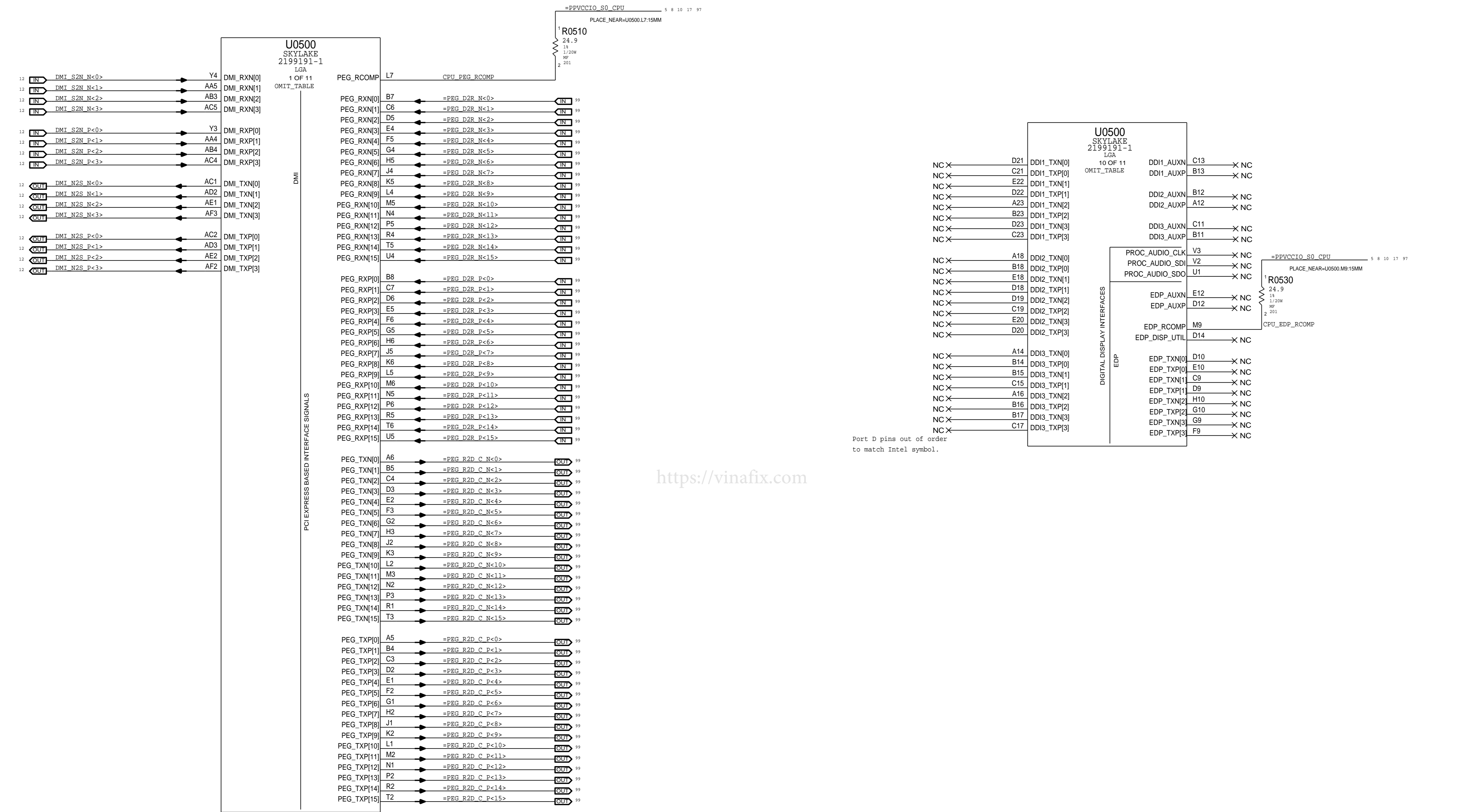
A

D

C

B

A



<https://vinafix.com>

Port D pins out of order to match Intel symbol.

BOM\_COST\_GROUP=CPU & CHIPSET

CPU & CHIPSET: CPU DMI/PEG/FDI/RSVD		DRAWING NUMBER	051-01506	SIZE	D
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