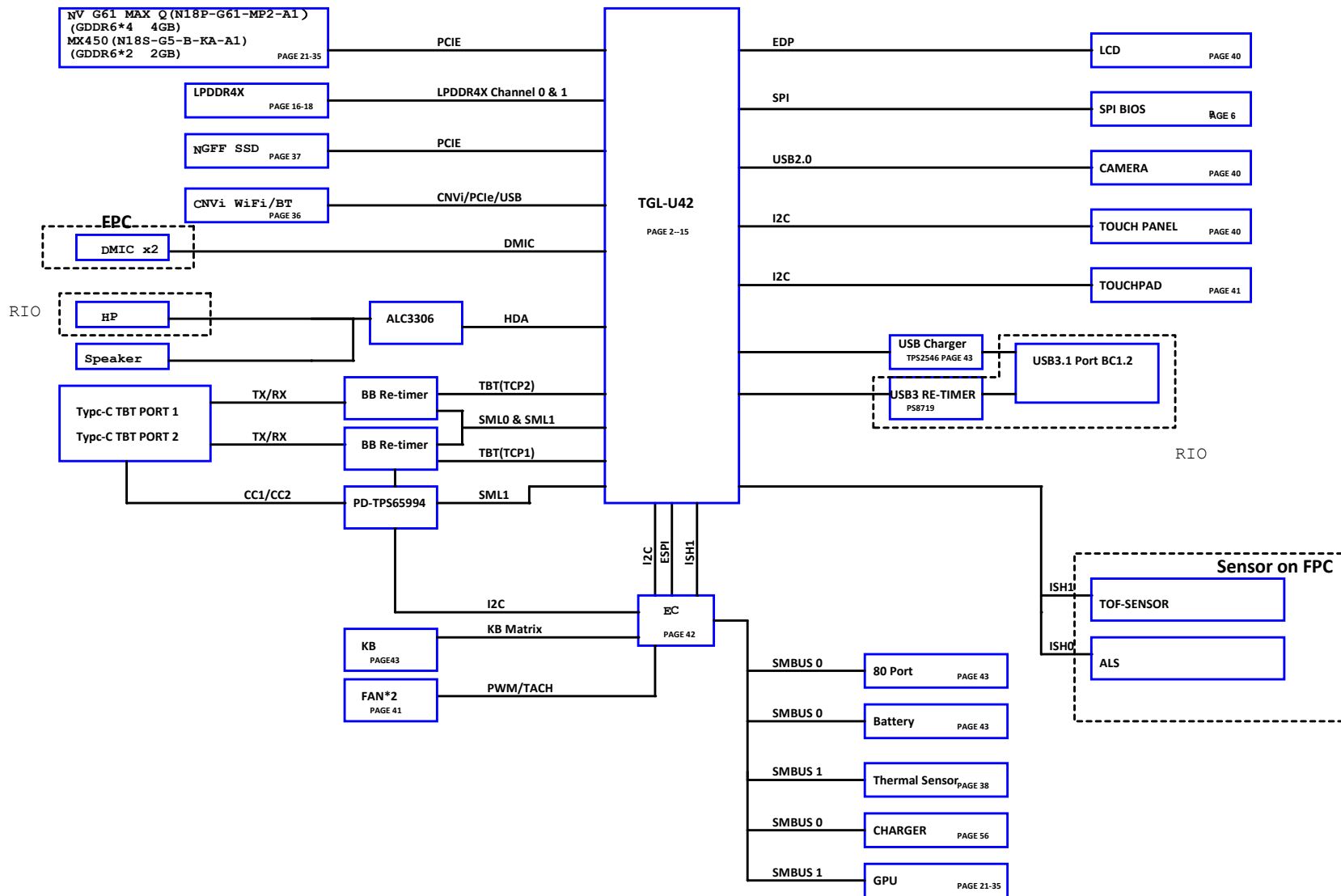
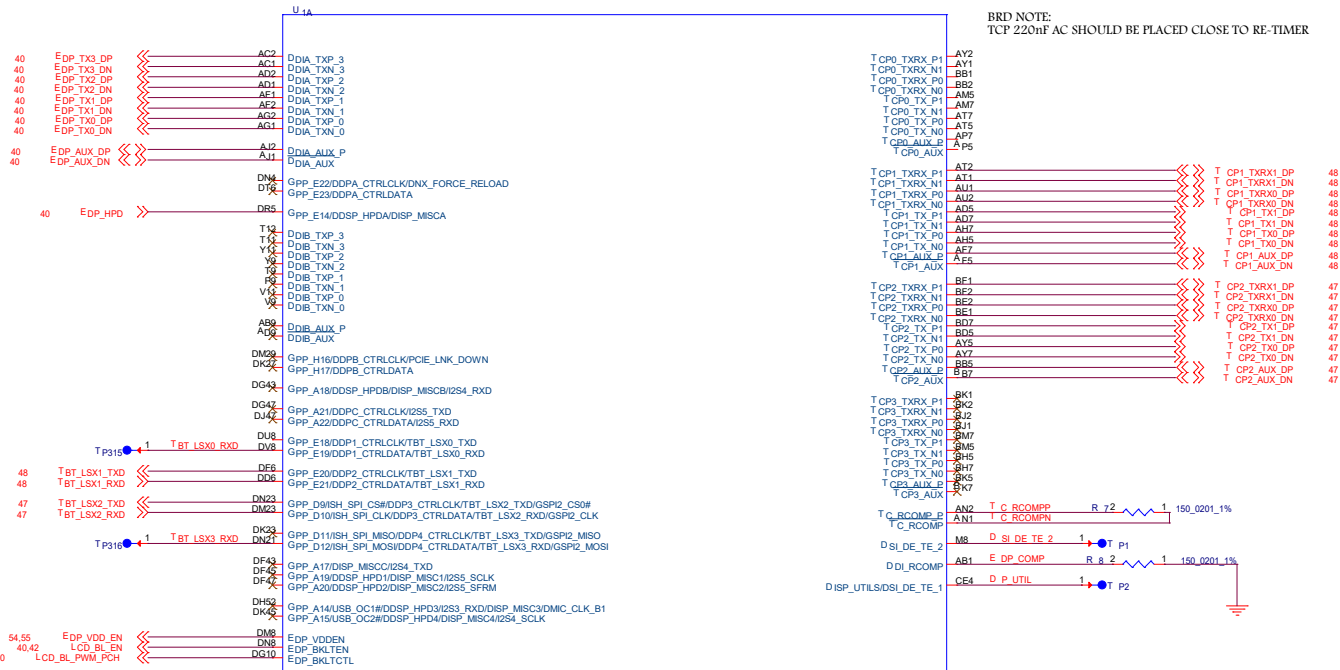


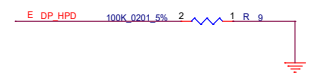
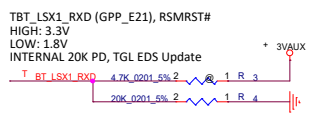
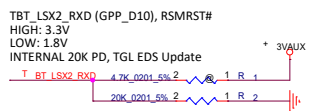
# S750 TGL-U42 Schematic Block Diagram



SMBus ADDRESS:  
 EC SMB1: 0x9A, 0x98 THERMAL SENSOR  
 I2C ADDRESS:  
 ISH-I2C0: 0x88 ALS  
 ISH I2C1: 0X52 TOF



BRD NOTE:  
TCP 220nF AC SHOULD BE PLACED CLOSE TO RE-TIMER



TGL\_U\_IP\_EXT  
@  
TGL\_NEED\_LCFC\_P/N

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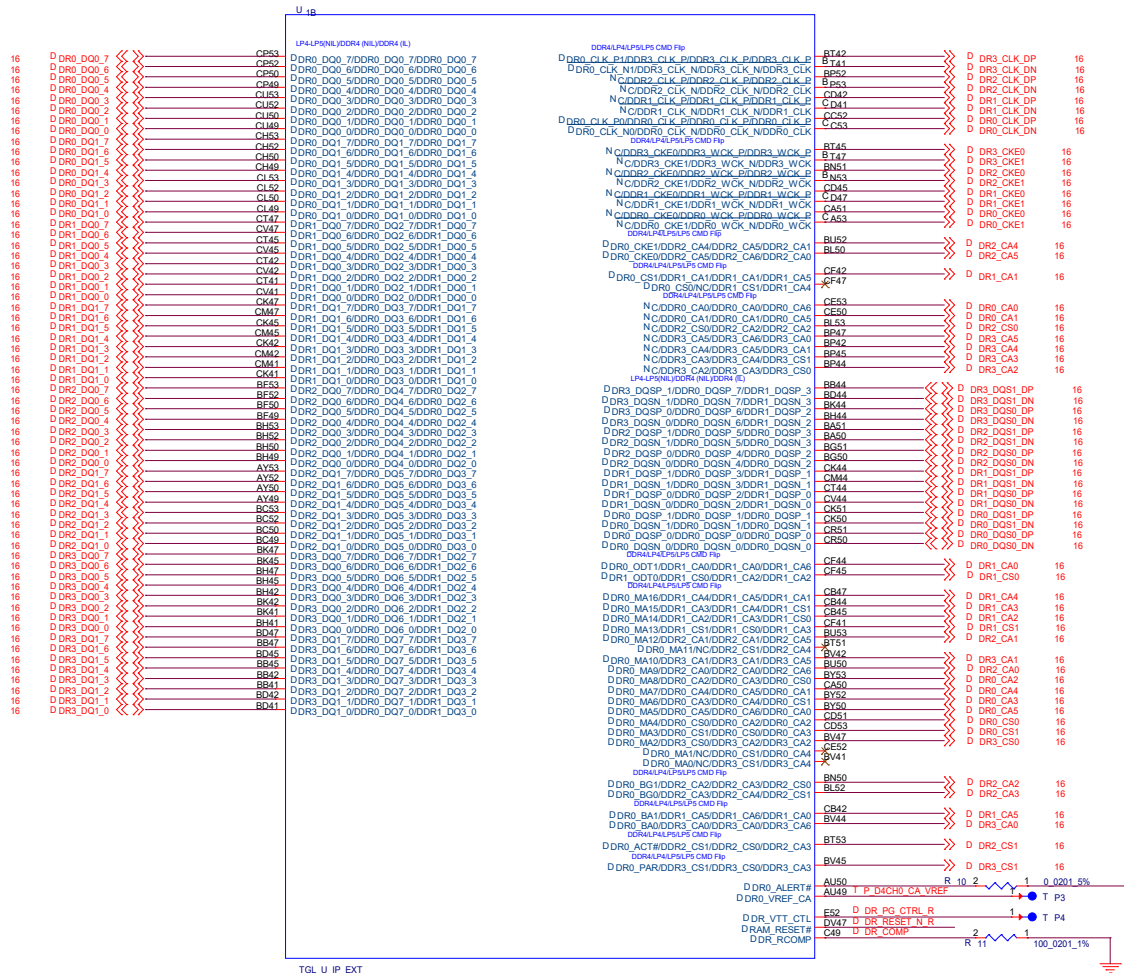
Title: T Iger Lake MCP (TBT,EDP)

Size C	Document Number S 750	Rev 0.5
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# CPU LPDDR4X CHANNEL 0



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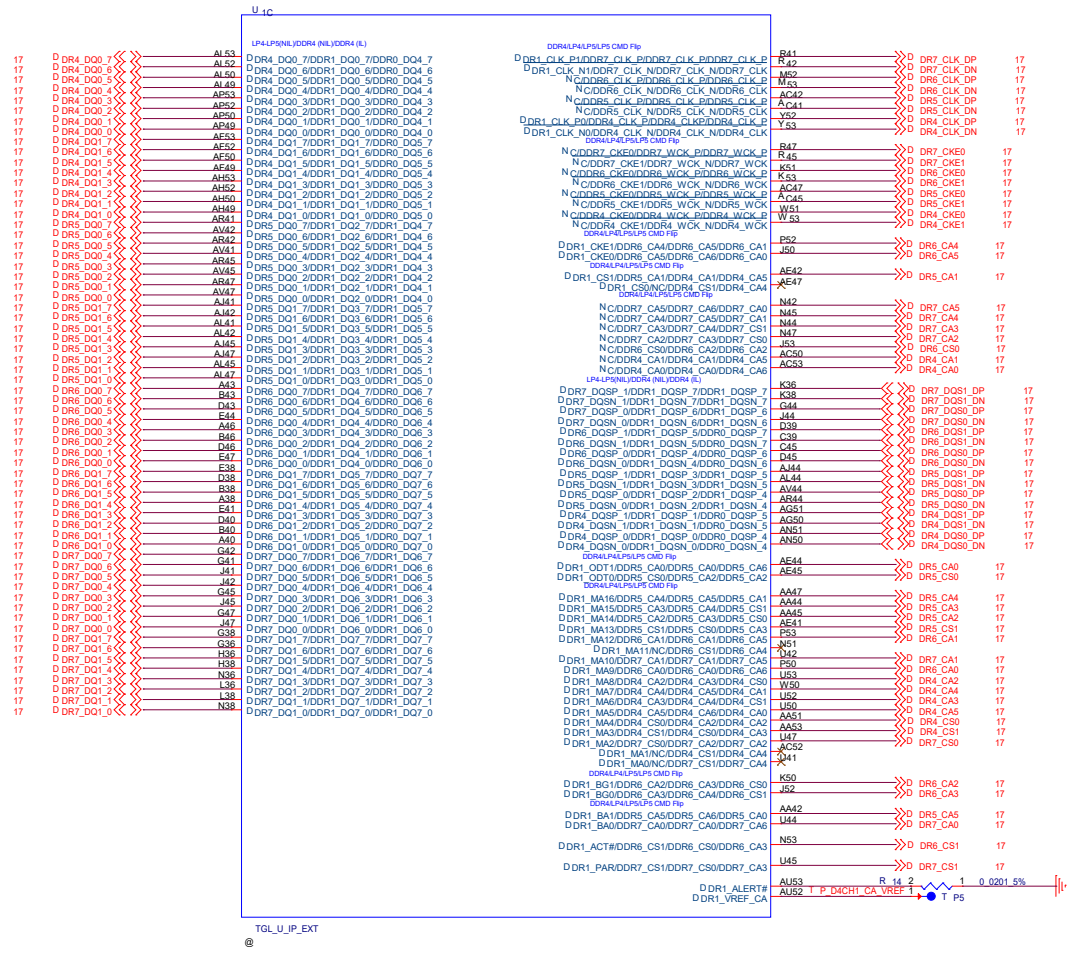
Title: Tiger Lake MCP ( PROCESSOR LP4X)

Size C	Document Number S 750	Rev 0.5
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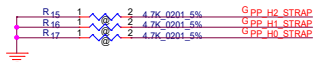
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# CPU LPDDR4X CHANNEL 1

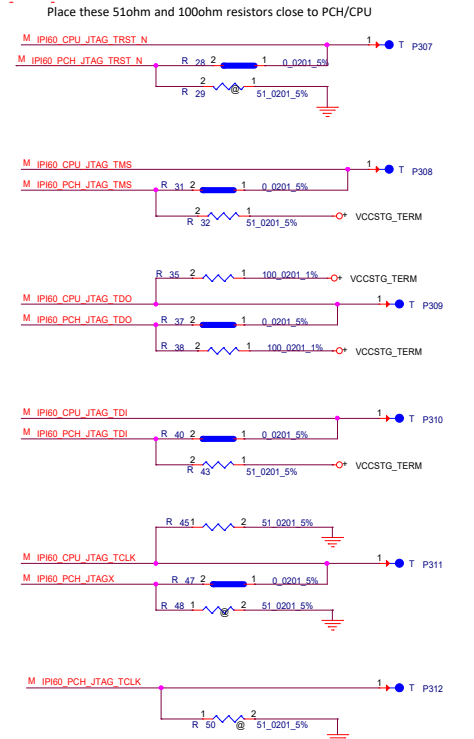
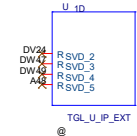
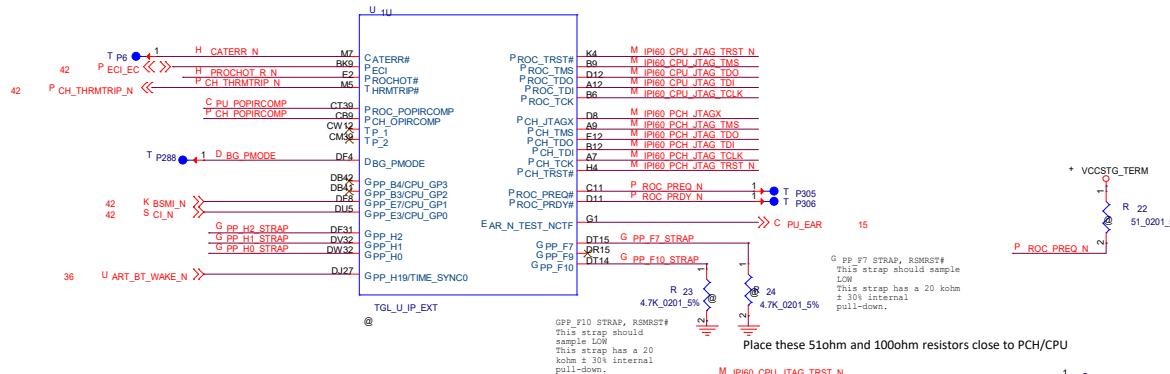
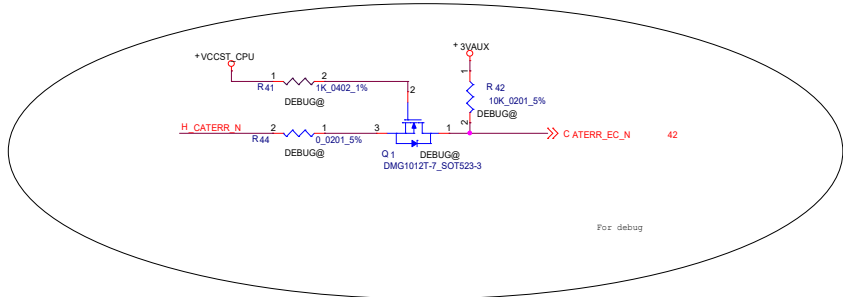
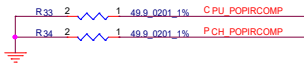
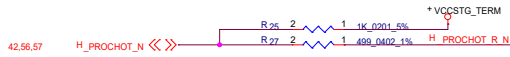
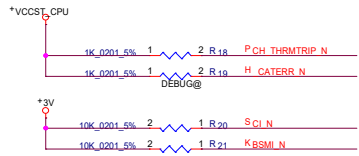


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Size	Document Number	Rev 0.5	
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GPP\_H0, GPP\_H1, GPP\_H2 (RSMRST#)  
 These signals have 20 K $\pm$  30% internal pull-down.  
 0 = Master Attached Flash Sharing (MAFS) is enabled.  
 (Default)  
 1 = Slave Attached Flash Sharing (SAFS) is enabled.  
 Notes:  
 1. The internal pull-down is disabled after RSMRST#  
 de-asserts.  
 2. This signal is in the primary well.



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